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Engineering Note

August 5, 1998
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CFT Axial Board
a980805a

Pinout of CFT backplane

rd, here's a 'work in progress' copy of the CFT Axial backplane pinout for Left and Right boards. It'll change as we go on, but needs to be saved e
ıyway.

LEFT

A	B	C	D	E
ION_OUT[19]	IN_FROM_PREV[47]	COMMON_TSTRB_OUT	IN_FROM_NEXT[47]	COMMON_OUT[9]
ION_OUT[18]	IN_FROM_PREV[46]	GND	IN_FROM_NEXT[46]	COMMON_OUT[8]
ION_OUT[17]	IN_FROM_PREV[45]	TSTRB_FROM_NEXT	IN_FROM_NEXT[45]	COMMON_OUT[7]
ION_OUT[16]	IN_FROM_PREV[44]	GND	IN_FROM_NEXT[44]	COMMON_OUT[6]
ION_OUT[15]	IN_FROM_PREV[43]	TSTRB_FROM_PREV	IN_FROM_NEXT[43]	COMMON_OUT[5]
ION_OUT[14]	IN_FROM_PREV[42]	GND	IN_FROM_NEXT[42]	COMMON_OUT[4]
ION_OUT[13]	IN_FROM_PREV[41]	RESERVED	IN_FROM_NEXT[41]	COMMON_OUT[3]
ION_OUT[12]	IN_FROM_PREV[40]	RESERVED	IN_FROM_NEXT[40]	COMMON_OUT[2]
ION_OUT[11]	IN_FROM_PREV[39]	GND	IN_FROM_NEXT[39]	COMMON_OUT[1]
ION_OUT[10]	IN_FROM_PREV[38]	RESERVED	IN_FROM_NEXT[38]	COMMON_OUT[0]
TO_PREV[27]	IN_FROM_PREV[37]	RESERVED	IN_FROM_NEXT[37]	OUT_TO_NEXT[27]
TO_PREV[26]	IN_FROM_PREV[36]	GND	IN_FROM_NEXT[36]	OUT_TO_NEXT[26]
TO_PREV[25]	IN_FROM_PREV[35]	GND	IN_FROM_NEXT[35]	OUT_TO_NEXT[25]
TO_PREV[24]	IN_FROM_PREV[34]	GND	IN_FROM_NEXT[34]	OUT_TO_NEXT[24]
TO_PREV[23]	IN_FROM_PREV[33]	GND	IN_FROM_NEXT[33]	OUT_TO_NEXT[23]
TO_PREV[22]	IN_FROM_PREV[32]	GND	IN_FROM_NEXT[32]	OUT_TO_NEXT[22]
TO_PREV[21]	IN_FROM_PREV[31]	GND	IN_FROM_NEXT[31]	OUT_TO_NEXT[21]
TO_PREV[20]	IN_FROM_PREV[30]	GND	IN_FROM_NEXT[30]	OUT_TO_NEXT[20]
TO_PREV[19]	IN_FROM_PREV[29]	RESERVED	IN_FROM_NEXT[29]	OUT_TO_NEXT[19]
TO_PREV[18]	IN_FROM_PREV[28]	RESERVED	IN_FROM_NEXT[28]	OUT_TO_NEXT[18]
TO_PREV[17]	IN_FROM_PREV[27]	+5V	IN_FROM_NEXT[27]	OUT_TO_NEXT[17]
TO_PREV[16]	IN_FROM_PREV[26]	+5V	IN_FROM_NEXT[26]	OUT_TO_NEXT[16]
TO_PREV[15]	IN_FROM_PREV[25]	+5V	IN_FROM_NEXT[25]	OUT_TO_NEXT[15]
TO_PREV[14]	IN_FROM_PREV[24]	+5V	IN_FROM_NEXT[24]	OUT_TO_NEXT[14]
TO_PREV[13]	IN_FROM_PREV[23]	+5V	IN_FROM_NEXT[23]	OUT_TO_NEXT[13]
TO_PREV[12]	IN_FROM_PREV[22]	+5V	IN_FROM_NEXT[22]	OUT_TO_NEXT[12]
TO_PREV[11]	IN_FROM_PREV[21]	+5V	IN_FROM_NEXT[21]	OUT_TO_NEXT[11]
TO_PREV[10]	IN_FROM_PREV[20]	+5V	IN_FROM_NEXT[20]	OUT_TO_NEXT[10]

ROM_RIGHT[7]	F_OUT_TO_RIGHT[7]	APWR	D_IN_FROM_RIGHT[7]	B_OUT_TO_RIGHT[7]
ROM_RIGHT[6]	F_OUT_TO_RIGHT[6]	APWR	D_IN_FROM_RIGHT[6]	B_OUT_TO_RIGHT[6]
ROM_RIGHT[5]	F_OUT_TO_RIGHT[5]	APWR	D_IN_FROM_RIGHT[5]	B_OUT_TO_RIGHT[5]
ROM_RIGHT[4]	F_OUT_TO_RIGHT[4]	APWR	D_IN_FROM_RIGHT[4]	B_OUT_TO_RIGHT[4]
ROM_RIGHT[3]	F_OUT_TO_RIGHT[3]	APWR	D_IN_FROM_RIGHT[3]	B_OUT_TO_RIGHT[3]
ROM_RIGHT[2]	F_OUT_TO_RIGHT[2]	APWR	D_IN_FROM_RIGHT[2]	B_OUT_TO_RIGHT[2]
ROM_RIGHT[1]	F_OUT_TO_RIGHT[1]	APWR	D_IN_FROM_RIGHT[1]	B_OUT_TO_RIGHT[1]
ROM_RIGHT[0]	F_OUT_TO_RIGHT[0]	APWR	D_IN_FROM_RIGHT[0]	B_OUT_TO_RIGHT[0]
ESERVED	RESERVED	RESERVED	RESERVED	RESERVED
ESERVED	RESERVED	RESERVED	RESERVED	RESERVED
ESERVED	RESERVED	RESERVED	RESERVED	RESERVED
RVED(CRYO)	RESERVED(CRYO)	AGND	D7	53_MHz
RVED(CRYO)	RESERVED(CRYO)	AGND	D6	53_MHz*
RVED(CRYO)	RESERVED(CRYO)	AGND	D5	GND
RVED(CRYO)	RESERVED(CRYO)	AGND	D4	CLK
RVED(CRYO)	RESERVED(CRYO)	AGND	D3	CLK*
RVED(CRYO)	RESERVED(CRYO)	AGND	D2	CROSSING
RVED(CRYO)	RESERVED(CRYO)	AGND	GND	RESET
RVED(CRYO)	RESERVED(CRYO)	AGND	D1	PRIORITY_IN
RVED(CRYO)	RESERVED(CRYO)	HDI_ENABLE	D0	CHANGE_MODE
RVED(CRYO)	RESERVED(CRYO)	DIR	DVALID	MODE1
RVED(CRYO)	RESERVED(CRYO)	VCAL	PRIORITY OUT	MODE0

RIGHT

	B	C	D	E
OUT[19]	IN_FROM_PREV[47]	COMMON_TSTRB_OUT	IN_FROM_NEXT[47]	COMMON_OUT[9]
OUT[18]	IN_FROM_PREV[46]	GND	IN_FROM_NEXT[46]	COMMON_OUT[8]
OUT[17]	IN_FROM_PREV[45]	TSTRB_FROM_NEXT	IN_FROM_NEXT[45]	COMMON_OUT[7]
OUT[16]	IN_FROM_PREV[44]	GND	IN_FROM_NEXT[44]	COMMON_OUT[6]
OUT[15]	IN_FROM_PREV[43]	TSTRB_FROM_PREV	IN_FROM_NEXT[43]	COMMON_OUT[5]
OUT[14]	IN_FROM_PREV[42]	GND	IN_FROM_NEXT[42]	COMMON_OUT[4]
OUT[13]	IN_FROM_PREV[41]	RESERVED	IN_FROM_NEXT[41]	COMMON_OUT[3]
OUT[12]	IN_FROM_PREV[40]	RESERVED	IN_FROM_NEXT[40]	COMMON_OUT[2]
OUT[11]	IN_FROM_PREV[39]	GND	IN_FROM_NEXT[39]	COMMON_OUT[1]
OUT[10]	IN_FROM_PREV[38]	RESERVED	IN_FROM_NEXT[38]	COMMON_OUT[0]
REV[27]	IN_FROM_PREV[37]	RESERVED	IN_FROM_NEXT[37]	OUT_TO_NEXT[27]
REV[26]	IN_FROM_PREV[36]	GND	IN_FROM_NEXT[36]	OUT_TO_NEXT[26]
REV[25]	IN_FROM_PREV[35]	GND	IN_FROM_NEXT[35]	OUT_TO_NEXT[25]
REV[24]	IN_FROM_PREV[34]	GND	IN_FROM_NEXT[34]	OUT_TO_NEXT[24]
REV[23]	IN_FROM_PREV[33]	GND	IN_FROM_NEXT[33]	OUT_TO_NEXT[23]

REV[1]	IN_FROM_PREV[11]	RESERVED	IN_FROM_NEXT[11]	OUT_TO_NEXT[1]
REV[0]	IN_FROM_PREV[10]	RESERVED	IN_FROM_NEXT[10]	OUT_TO_NEXT[0]
VED	IN_FROM_PREV[9]	RESERVED	IN_FROM_NEXT[9]	RESERVED
VED	IN_FROM_PREV[8]	RESERVED	IN_FROM_NEXT[8]	RESERVED
COM[1]	IN_FROM_PREV[7]	COMMON_ISTRB_OUT	IN_FROM_NEXT[7]	ISO_OUT_COM[0]
PREV[1]	IN_FROM_PREV[6]	RESERVED	IN_FROM_NEXT[6]	ISO_FROM_NEXT[1]
PREV[0]	IN_FROM_PREV[5]	RESERVED	IN_FROM_NEXT[5]	ISO_FROM_NEXT[0]
M_PREV	IN_FROM_PREV[4]	RESERVED	IN_FROM_NEXT[4]	ISTRB_FROM_NEXT
VED	IN_FROM_PREV[3]	RESERVED	IN_FROM_NEXT[3]	RESERVED
VED	IN_FROM_PREV[2]	RESERVED	IN_FROM_NEXT[2]	RESERVED
PREV[0]	IN_FROM_PREV[1]	RESERVED	IN_FROM_NEXT[1]	IN_FROM_NEXT[0]

LEFT[7]	F_IN_FROM_LEFT[7]	APWR	D_OUT_TO_LEFT[7]	B_IN_FROM_LEFT[7]
LEFT[6]	F_IN_FROM_LEFT[6]	APWR	D_OUT_TO_LEFT[6]	B_IN_FROM_LEFT[6]
LEFT[5]	F_IN_FROM_LEFT[5]	APWR	D_OUT_TO_LEFT[5]	B_IN_FROM_LEFT[5]
LEFT[4]	F_IN_FROM_LEFT[4]	APWR	D_OUT_TO_LEFT[4]	B_IN_FROM_LEFT[4]
LEFT[3]	F_IN_FROM_LEFT[3]	APWR	D_OUT_TO_LEFT[3]	B_IN_FROM_LEFT[3]
LEFT[2]	F_IN_FROM_LEFT[2]	APWR	D_OUT_TO_LEFT[2]	B_IN_FROM_LEFT[2]
LEFT[1]	F_IN_FROM_LEFT[1]	APWR	D_OUT_TO_LEFT[1]	B_IN_FROM_LEFT[1]
LEFT[0]	F_IN_FROM_LEFT[0]	APWR	D_OUT_TO_LEFT[0]	B_IN_FROM_LEFT[0]
VED	RESERVED	RESERVED	RESERVED	RESERVED
VED	RESERVED	RESERVED	RESERVED	RESERVED
VED	RESERVED	RESERVED	RESERVED	RESERVED

Hz	D7	AGND	RESERVED(CRYO)	RESERVED(CRYO)
Hz*	D6	AGND	RESERVED(CRYO)	RESERVED(CRYO)
	D5	AGND	RESERVED(CRYO)	RESERVED(CRYO)
	D4	AGND	RESERVED(CRYO)	RESERVED(CRYO)
	D3	AGND	RESERVED(CRYO)	RESERVED(CRYO)
ING	D2	AGND	RESERVED(CRYO)	RESERVED(CRYO)
T	GND	AGND	RESERVED(CRYO)	RESERVED(CRYO)
Y_IN	D1	AGND	RESERVED(CRYO)	RESERVED(CRYO)
MODE	D0	HDI_ENABLE	RESERVED(CRYO)	RESERVED(CRYO)
E1	DVALID	DIR	RESERVED(CRYO)	RESERVED(CRYO)
E0	PRIORITY OUT	VCAL	RESERVED(CRYO)	RESERVED(CRYO)