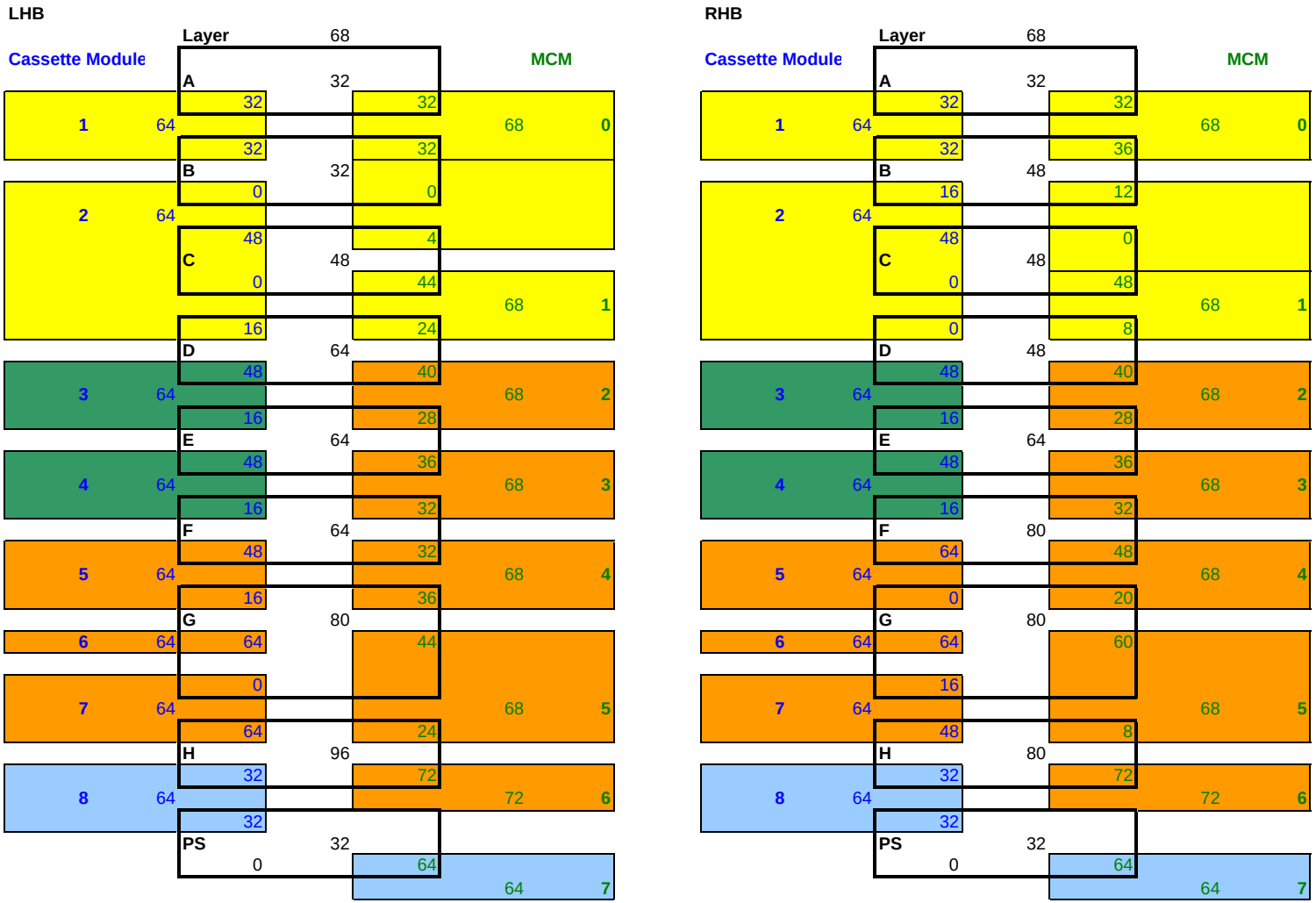


The number of channels from layers B, D, F and H is exchanged between the LH and RH boards on a cassette to make the smalles cassette connector 16 channels.
The bias splits at the D layer on the LHB and at the B layer on the RHB. The thresholds for both split in the D layer.

**Figure A 1**

**LHB**

| Cassette Module | | Layer | | | | MCM | |
|---|---|---|---|---|---|---|---|
| | | | 68 | | | | |
| | | 1 - 32 | 32 | | | | |
| | | 32 | | 32 | | | |
| 1 | 64 | | | | | 68 | 0 |
| | | 32 | | 32 | | | |
| | | 33 - 64 | 32 | | | | |
| | | 0 | | 0 | | | |
| 2 | 64 | | | | | | |
| | | 48 | | 4 | | | |
| | | 65 - 112 | 48 | | | | |
| | | 0 | | 44 | | | |
| | | | | | | 68 | 1 |
| | | 16 | | 24 | | | |
| | | 113 - 176 | 64 | | | | |
| | | 48 | | 40 | | | |
| 3 | 64 | | | | | 68 | 2 |
| | | 16 | | 28 | | | |
| | | 177 - 240 | 64 | | | | |
| | | 48 | | 36 | | | |
| 4 | 64 | | | | | 68 | 3 |
| | | 16 | | 32 | | | |
| | | 241 - 256 | | | | | |
| | | 1 - 48 | 64 | | | | |
| | | 48 | | 32 | | | |
| 5 | 64 | | | | | 68 | 4 |
| | | 16 | | 36 | | | |
| | | 49 - 128 | 80 | | | | |
| 6 | 64 | 64 | | 44 | | | |
| | | 0 | | | | | |
| 7 | 64 | | | | | 68 | 5 |
| | | 64 | | 24 | | | |
| | | 129 - 224 | 96 | | | | |
| | | 32 | | 72 | | | |
| 8 | 64 | | | | | 72 | 6 |
| | | 32 | | | | | |
| | | 225 - 256 | 32 | | | | |
| | | 0 | | 32 | | | |
| | | | | | | 32 | 7 |

**RHB**

| Cassette Module | | Layer | | | | MCM | |
|---|---|---|---|---|---|---|---|
| | | | 68 | | | | |
| | | ’1 - 32 | 32 | | | | |
| | | 32 | | 32 | | | |
| 1 | 64 | | | | | 68 | 0 |
| | | 32 | | 36 | | | |
| | | 33 - 80 | 48 | | | | |
| | | 16 | | 12 | | | |
| 2 | 64 | | | | | | |
| | | 48 | | 0 | | | |
| | | 81 - 128 | 48 | | | | |
| | | 0 | | 48 | | | |
| | | | | | | 68 | 1 |
| | | 0 | | 8 | | | |
| | | 129 - 176 | 48 | | | | |
| | | 48 | | 40 | | | |
| 3 | 64 | | | | | 68 | 2 |
| | | 16 | | 28 | | | |
| | | 177 - 240 | 64 | | | | |
| | | 48 | | 36 | | | |
| 4 | 64 | | | | | 68 | 3 |
| | | 16 | | 32 | | | |
| | | 241 - 256 | | | | | |
| | | 1 - 64 | 80 | | | | |
| | | 64 | | 48 | | | |
| 5 | 64 | | | | | 68 | 4 |
| | | 0 | | 20 | | | |
| | | 65 - 144 | 80 | | | | |
| 6 | 64 | 64 | | 60 | | | |
| | | 16 | | | | | |
| 7 | 64 | | | | | 68 | 5 |
| | | 48 | | 8 | | | |
| | | 145 - 224 | 80 | | | | |
| | | 32 | | 72 | | | |
| 8 | 64 | | | | | 72 | 6 |
| | | 32 | | | | | |
| | | 225 - 256 | 32 | | | | |
| | | 0 | | 32 | | | |
| | | | | | | 32 | 7 |

The channel layout for the CFT Stereo Boards using the same board layout as the CFT Axial Trigger boards.
The same board is used for the stereo. When it is stuffed the charge division for the 32 formerly PS channels is modified to non-charge division.

**Figure A 2**

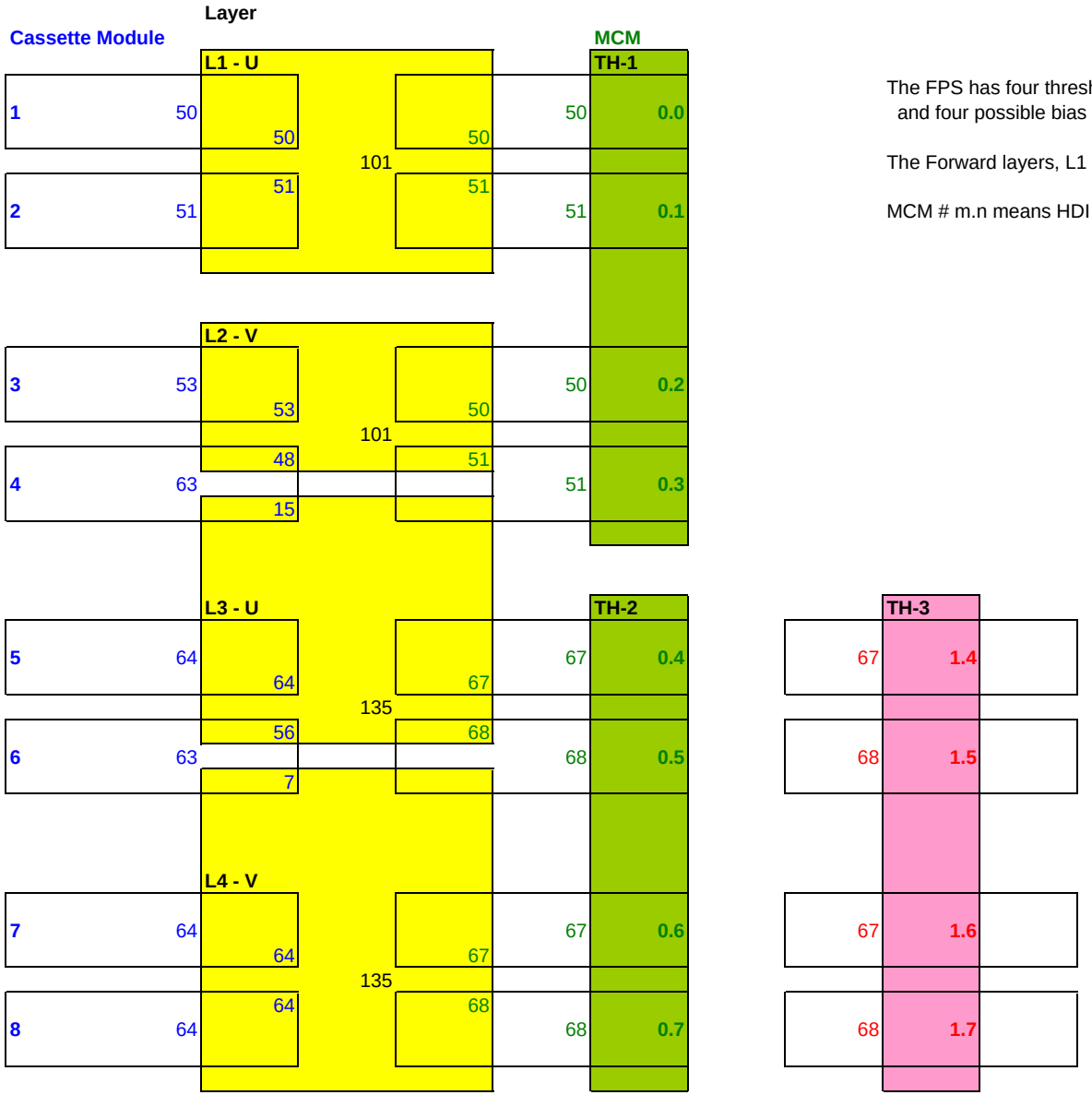


Figure A 3

The FPS has four thresholds
and four possible bias values

The Forward layers, L1 and L2 have NO charge splitting.

MCM # m.n means HDI # m and MCM/SVX # n

*512*

**Figure A 4**

The CPS Stereo has four thresholds and four possible bias values.

MCM # m.n means HDI # m and MCM/SVX # n