

# Online Software Status Report

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# Online Software Status

## Current Projects

- EPICS Slow Controls (McDonald + Gilmartin + Bartlett)
  - VxWorks MIL-1553 driver - completed
  - EPICS MIL-1553 driver - started
  - Generic MIL-1553 record - completed
    - ▲ Download to SVX sequencer board - tested
    - ▲ Interface to rack monitor tested - tested
  - Rack monitor record and device support design - started
  - Generic rack monitor user interface (GUI) - started
  - SVX sequencer record and device support - not started

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## Current Projects

- DART Secondary Data Acquisition (Krzywdzinski)
  - Support for SMT testing
    - ▲ Run suspension by IOC process - **completed**
    - ▲ Sequencing of run series - **completed**
    - ▲ TCL interface to EPICS Channel Access - **started**
    - ▲ PYTHON interface to DART processes - **not started**
  - Data acquisition from multiple IOC's - **started**
    - ▲ Waiting for completion by CD
- Clock Subsystem (Paterno) - **started**
  - Design of low-level interface to hardware - shared with CDF

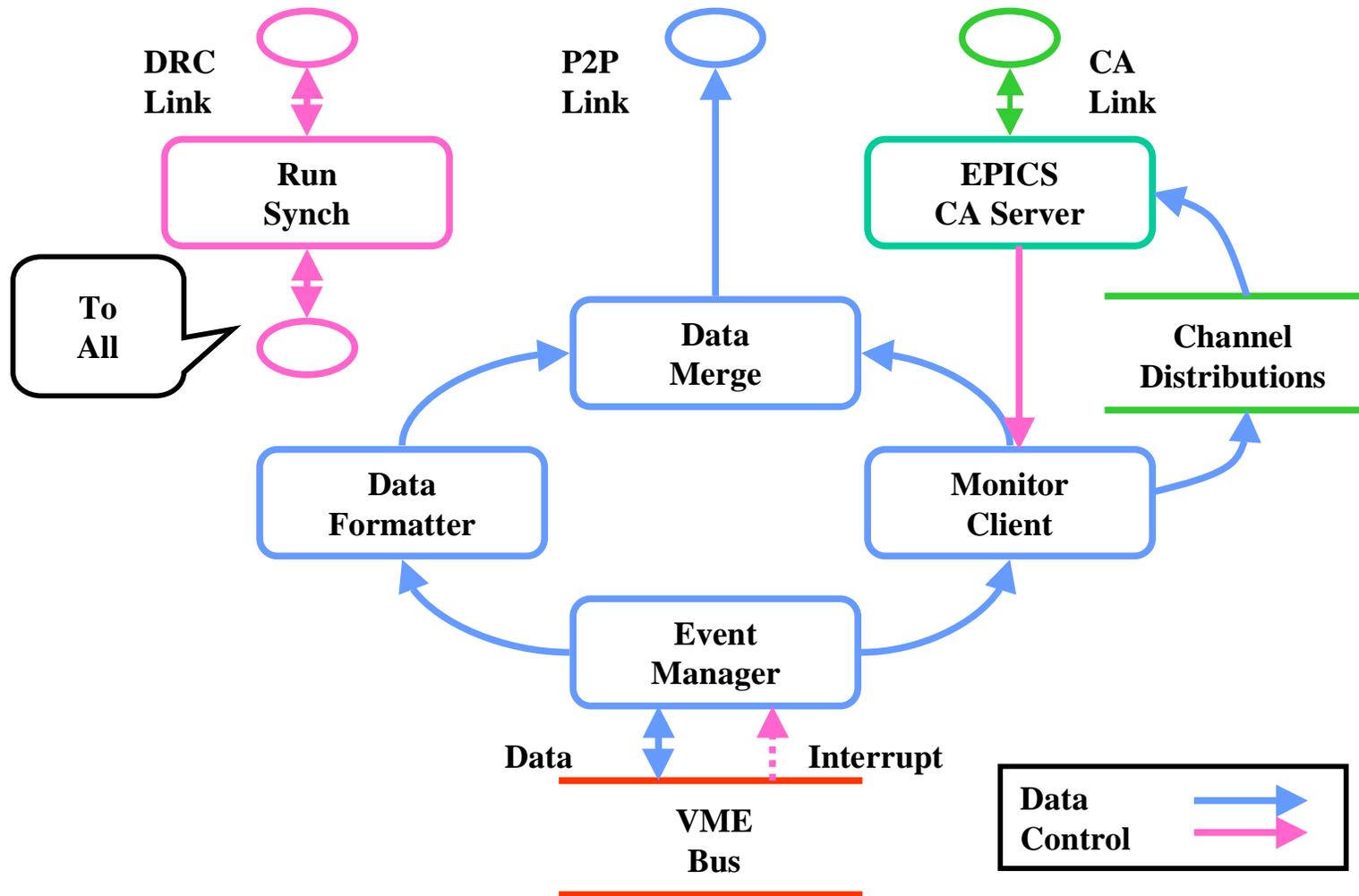
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## Current Projects

- Control Database (Krzywdzinski)
  - Conversion of RDB database from Run I to Oracle database for Run II - **started**
- High Voltage Subsystem (Bartlett + McDonald)
  - Required mid-September - **not started**

# Online Software Status

## DØ Run II IOC Software



August 24, 1998