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Engineering Note

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Subject: Jumper Settings Sequencer and Controller for 1553 FPGA Programming

This note shows jumper (shunt) placement on the Sequencer or Sequencer Controller boards when using either a PROM or Byteblaster to program the 1553 FPGA upon power up. When power is off, the FPGA loses its programming information. If a PROM is used, the firmware is immediately and automatically programmed into the FPGA just after power is applied. When a Byteblaster is used, we first turn on power, then plug in the Byteblaster and load the proper .sof file via the MaxPlus software.

Note that Pin 1 has a square solder pad and pins 2 and 3 have round solder pads.

Jumper Settings for PROM initialization of 1553 FPGA

Jmp1	1-2
Jmp2	2-3
Jmp3	2-3
Jmp4	2-3

Jumper Settings for Byteblaster initialization of 1553 FPGA

Note: Remove PROM!

Jmp1	OFF
Jmp2	2-3
Jmp3	2-3
Jmp4	1-2