

# The BROADCASTER

## communication protocols and data encoding

A PROPOSAL BY

Manuel I. Martin

for the DØ Trigger

October 1998

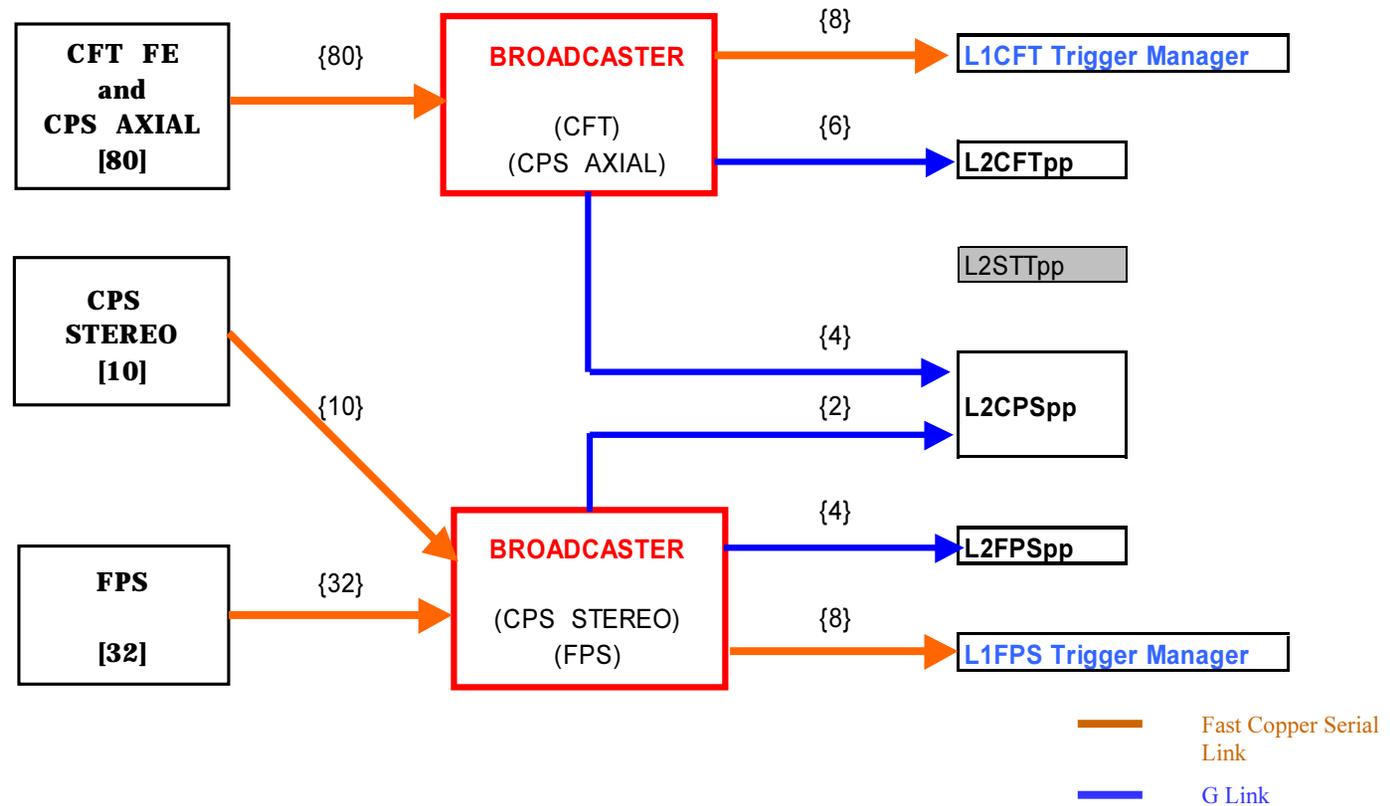
Proposal by M. I. Martin

# CFT ARCHITECTURE

and

# DATA FLOW to L1 and L2

## LINKS between FE and BROADCASTER BROADCASTER and TRIGGERS



October 1998

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# CFT ARCHITECTURE

and

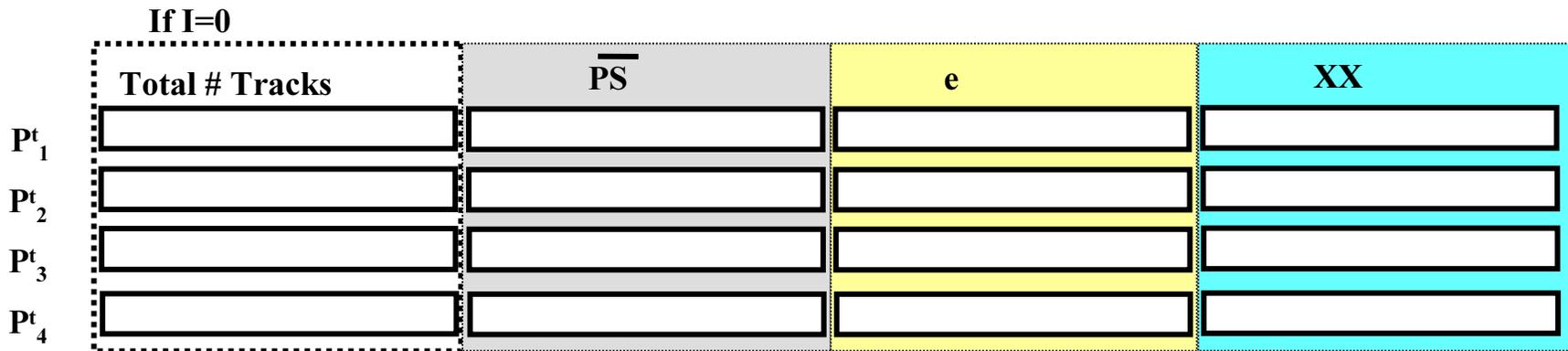
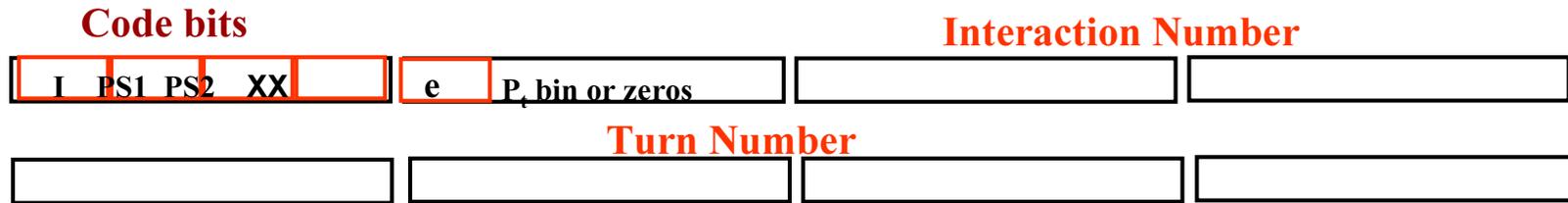
# DATA FLOW to L1 and L2

*For a System as complex as the Central Tracker Trigger, the definition of the Hardware Architecture is not sufficient to ensure good performance. A good definition of "ALL" the codes used to transfer data from one Functional Block to another is also necessary. This is imperative when different groups are responsible for the design of different Blocks. Normally this is the responsibility of the System Designer. As nobody had taken this responsibility, I make a Proposal for a cohesive way of dealing with the Data Flow between main Functional Blocks. I will use as starting point the Data Encoding for the FE to the Broadcaster that I presented long ago.*

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Proposal by M. I. Martin

# Data Codes proposed for the transfer of L1 data from the FE to the Broadcaster (presented long ago)



Equivalent to 4 bits

# Data Codes proposed for the transfer of L1 data from the Broadcaster to the CFTTM (presented long ago)

## Encoding of:

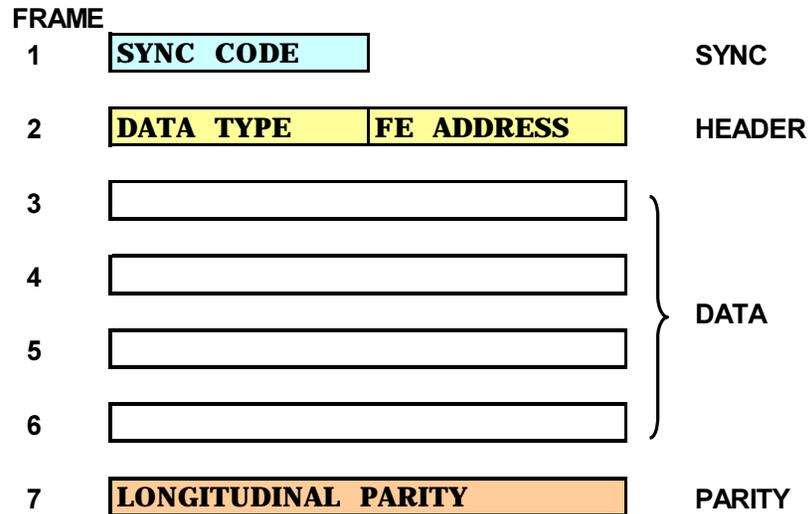
- Header.... **Error**(1bit), **LSB of Event #** (up to 3 bits)
- Tracks with **Tag X** (2 bits per Pt bin)
- Total** Tracks (6 bits per Pt bin)
- Tracks with **PS** tag (6 bits per Pt bin)
- Electron** candidates (6 bits per Pt bin)
- Isolated** Tracks (3 bits per Pt bin)

<b>Header</b>			
<b>Tag X (Pt 1)</b>	<b>Tag X (Pt 2)</b>	<b>Tag X (Pt 3)</b>	<b>Tag X (Pt 4)</b>
<b>Total # of Tracks (Pt 1)</b>		<b>Total # of Tracks (Pt 2)</b>	
<b>Total # of Tracks (Pt 3)</b>		<b>Total # of Tracks (Pt 4)</b>	
<b>Tracks with PS (Pt 1)</b>		<b>Tracks with PS (Pt 2)</b>	
<b>Tracks with PS (Pt 3)</b>		<b>Tracks with PS (Pt 4)</b>	
<b>e candidates (Pt 1)</b>		<b>e candidates (Pt 2)</b>	
<b>e candidates (Pt 3)</b>		<b>e candidates (Pt 4)</b>	
<b>Isolated (Pt 1)</b>	<b>Isolated (Pt 1)</b>	<b>Isolated (Pt 1)</b>	<b>Isolated (Pt 1)</b>

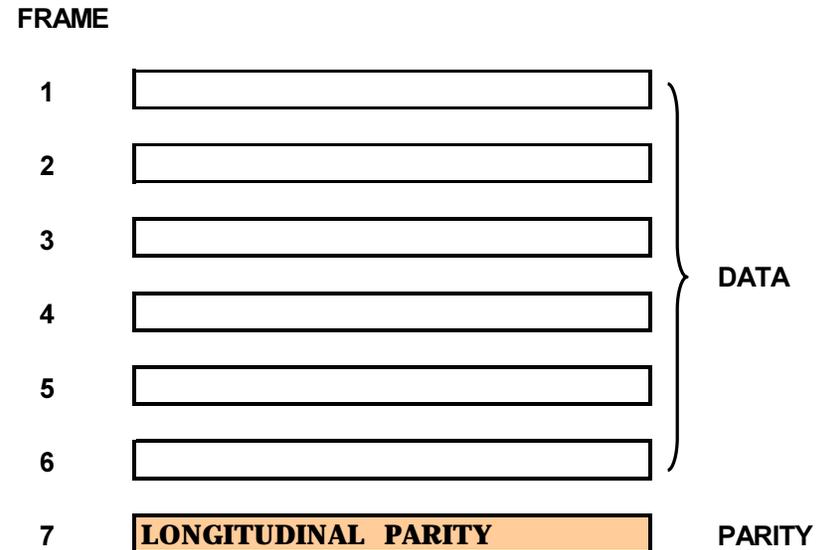
# L1

## PROTOCOLS for the Fast Cu Serial Link

### FE to BROADCASTER

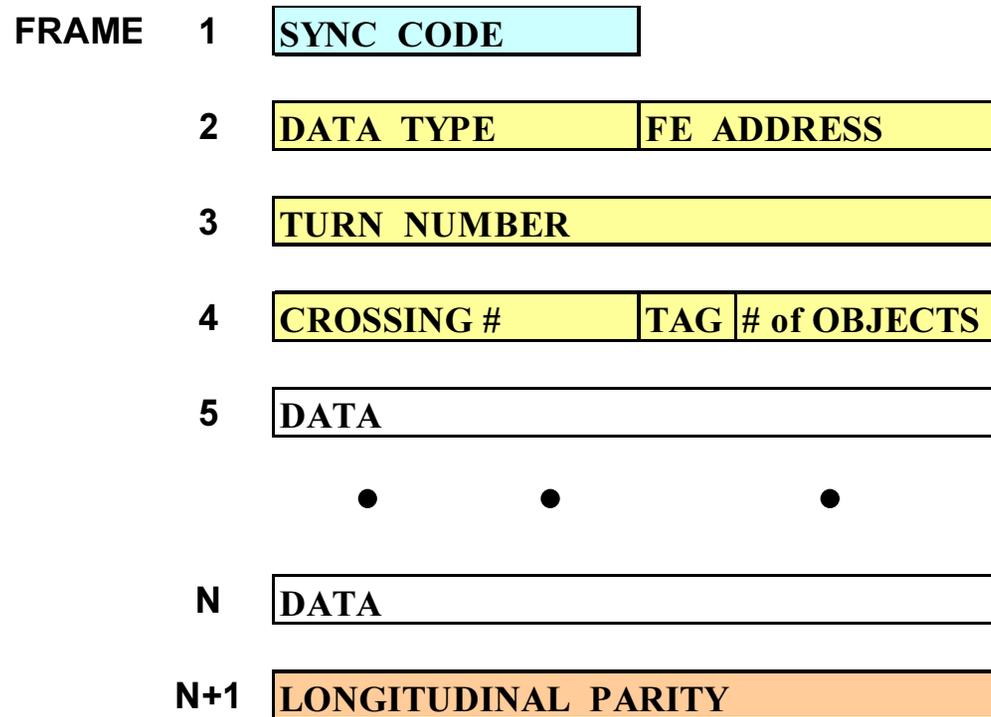


### BROADCASTER to Trigger Manager (L1) (same as FE to $\mu$ )



Each Frame consists of 16 bits

# L2 PROTOCOLS for the Fast Cooper Serial Link



**Each Frame consists of 16 bits**