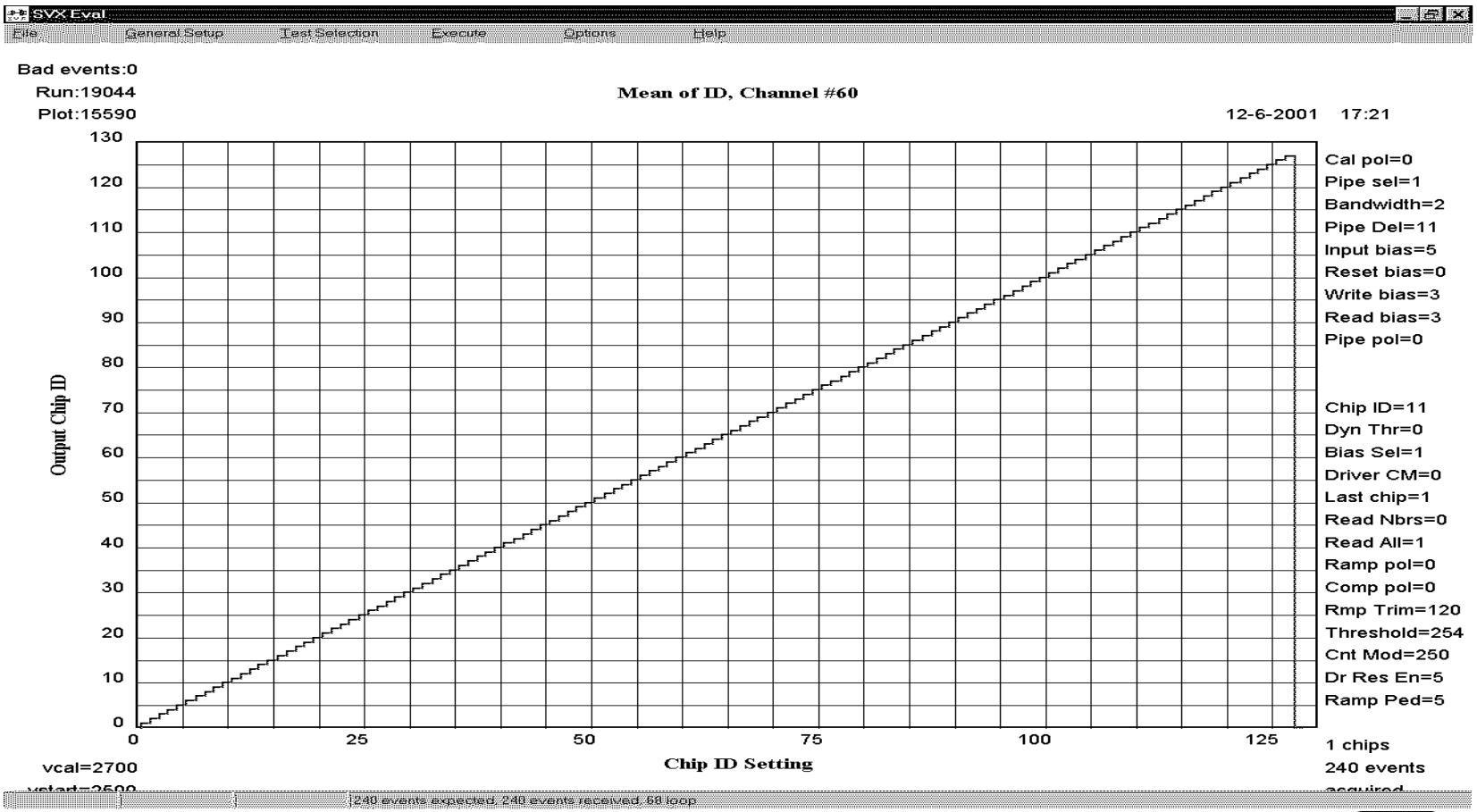
SVX4 Test Stand

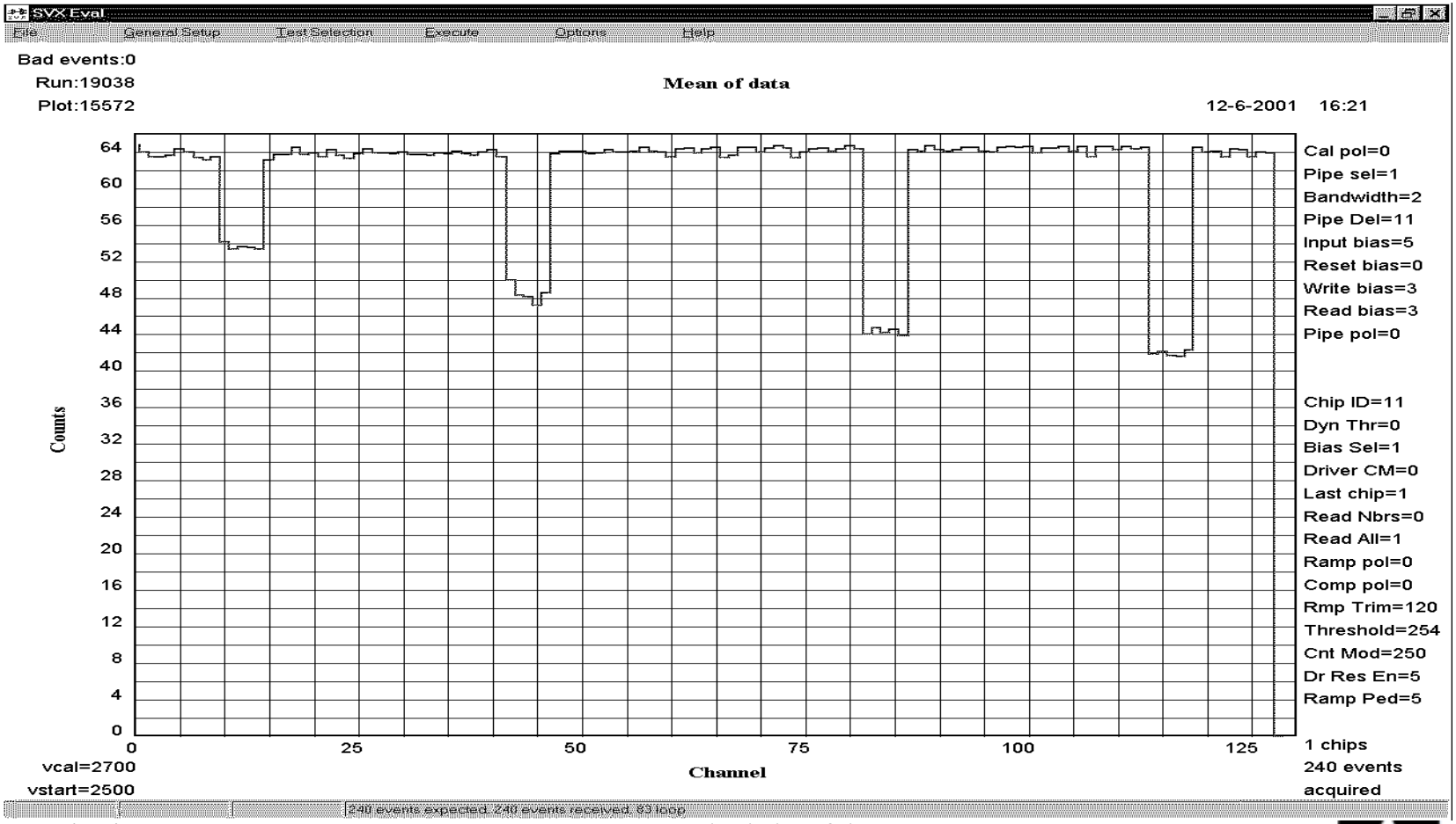
SVX3 Test Stand Status

- Running SVX3 in SVX2 mode with single and double chip hybrid (card carrier)
- Running SVX3 in SVX3 mode (Readout with Acquire and Digitize occurring simultaneously)
- Relocation? Anytime is fine with me.

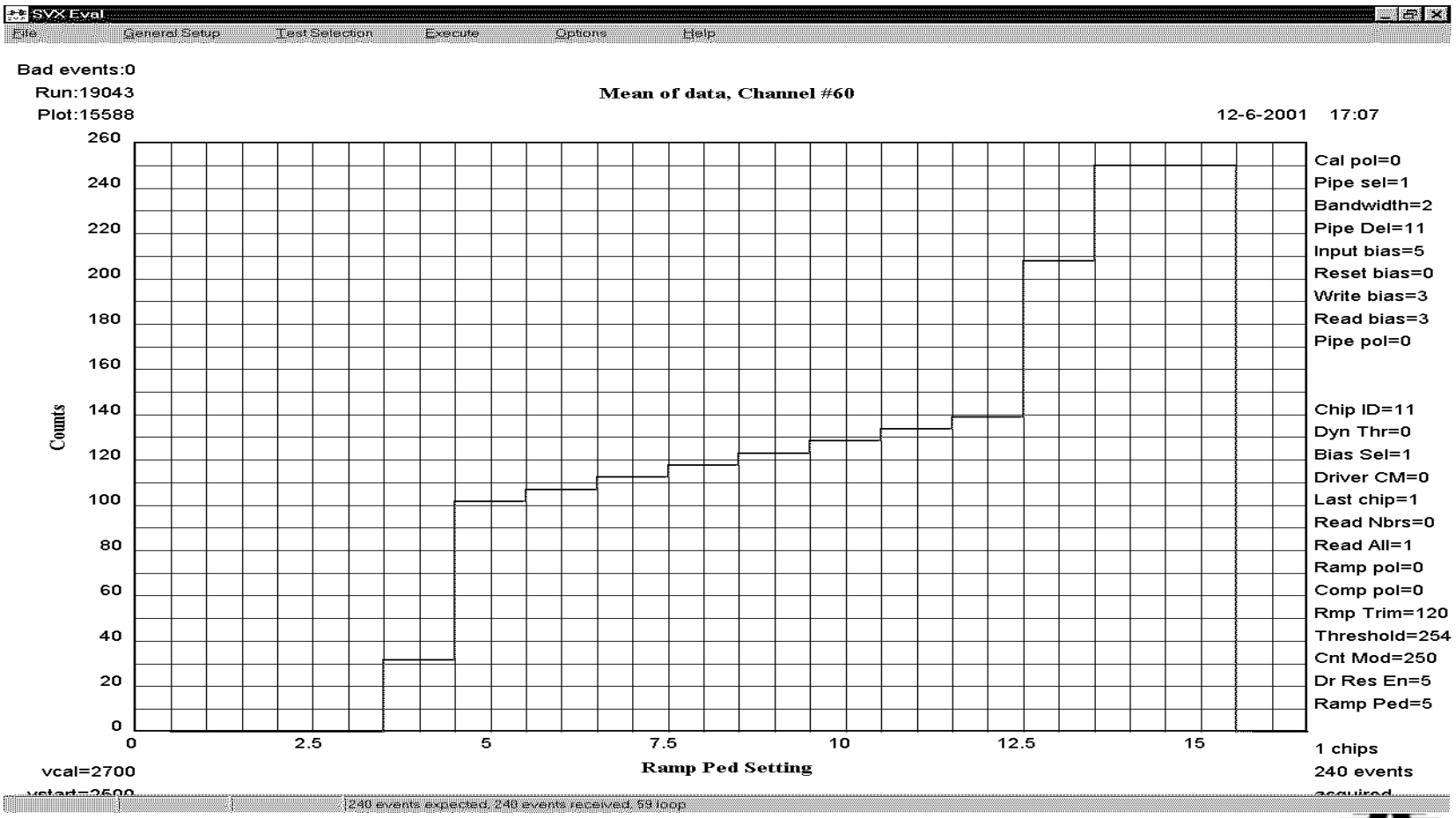
Channel ID


SVX Eval
File
General Setup
Test Selection
Execute
Options
Help
Bad events:0
Run:19044
Plot:15590
Mean of ID, Channel #60
12-6-2001 17:21
Output Chip ID
Chip ID Setting
0
10
20
30
40
50
60
70
80
90
100
110
120
130
0
25
50
75
100
125
Cal pol=0
Pipe sel=1
Bandwidth=2
Pipe Del=11
Input bias=5
Reset bias=0
Write bias=3
Read bias=3
Pipe pol=0
Chip ID=11
Dyn Thr=0
Bias Sel=1
Driver CM=0
Last chip=1
Read Nbrs=0
Read All=1
Ramp pol=0
Comp pol=0
Rmp Trim=120
Threshold=254
Cnt Mod=250
Dr Res En=5
Ramp Ped=5
1 chips
240 events
vcal=2700
240 events expected, 240 events received, 60 loop

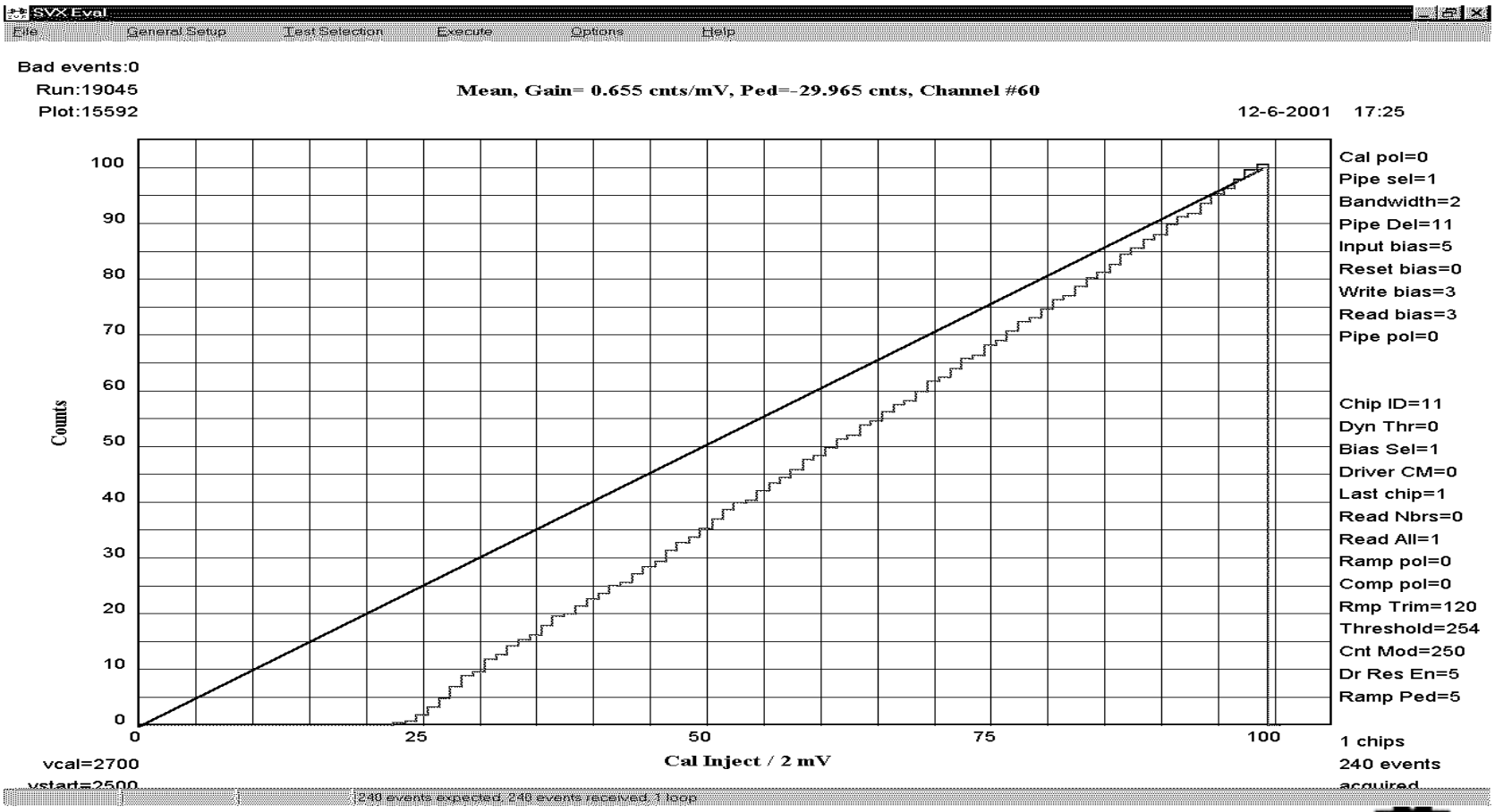
Pedestals


SVX Eval
File
General Setup
Test Selection
Execute
Options
Help
Bad events:0
Run:19038
Plot:15572
Mean of data
12-6-2001 16:21
Counts
64
60
56
52
48
44
40
36
32
28
24
20
16
12
8
4
0
0
25
50
75
100
125
Channel
vcal=2700
vstart=2500
Cal pol=0
Pipe sel=1
Bandwidth=2
Pipe Del=11
Input bias=5
Reset bias=0
Write bias=3
Read bias=3
Pipe pol=0
Chip ID=11
Dyn Thr=0
Bias Sel=1
Driver CM=0
Last chip=1
Read Nbrs=0
Read All=1
Ramp pol=0
Comp pol=0
Rmp Trim=120
Threshold=254
Cnt Mod=250
Dr Res En=5
Ramp Ped=5
1 chips
240 events
acquired
240 events expected, 240 events received, 93 loop

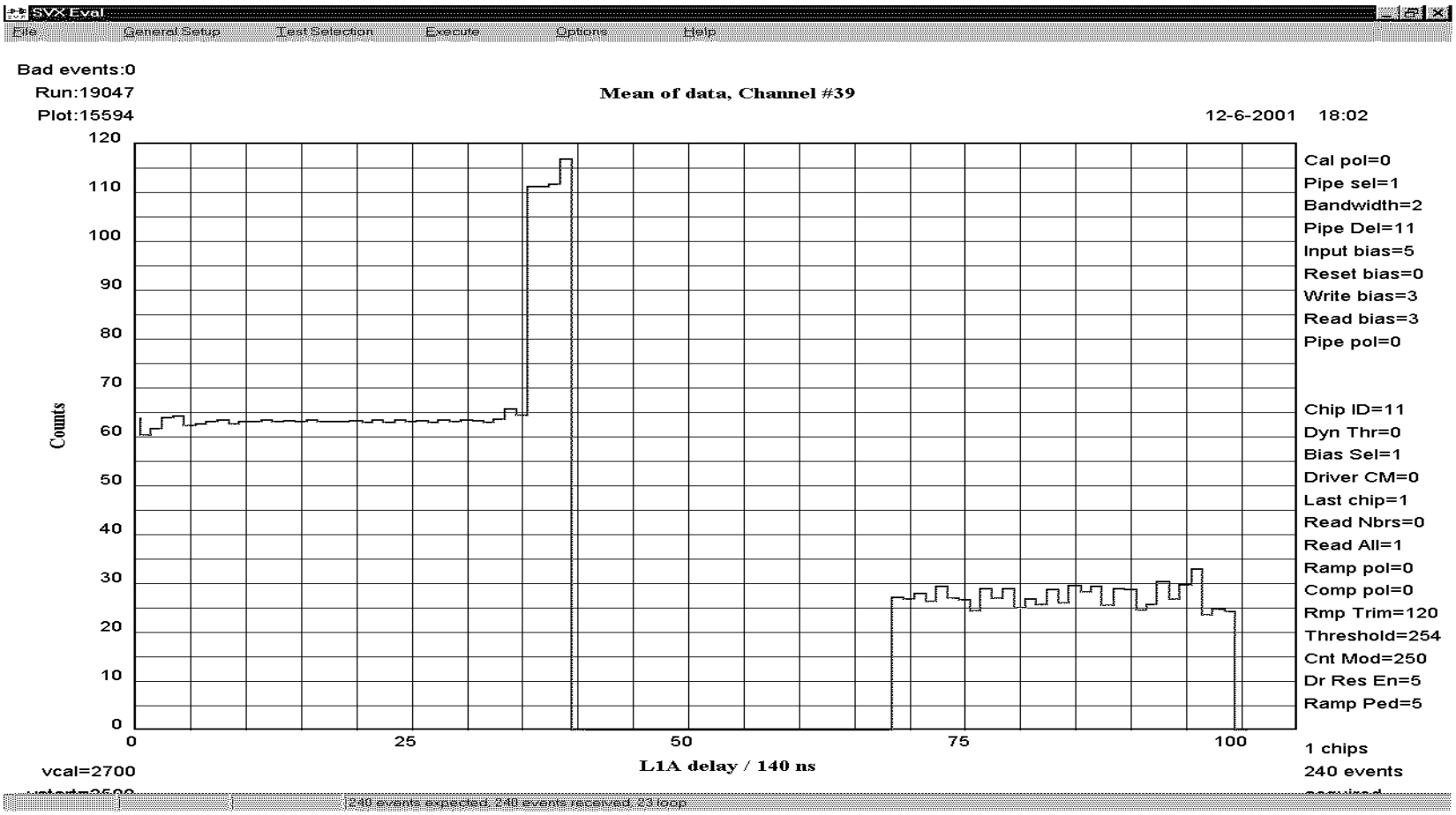
Ramp Pedastals



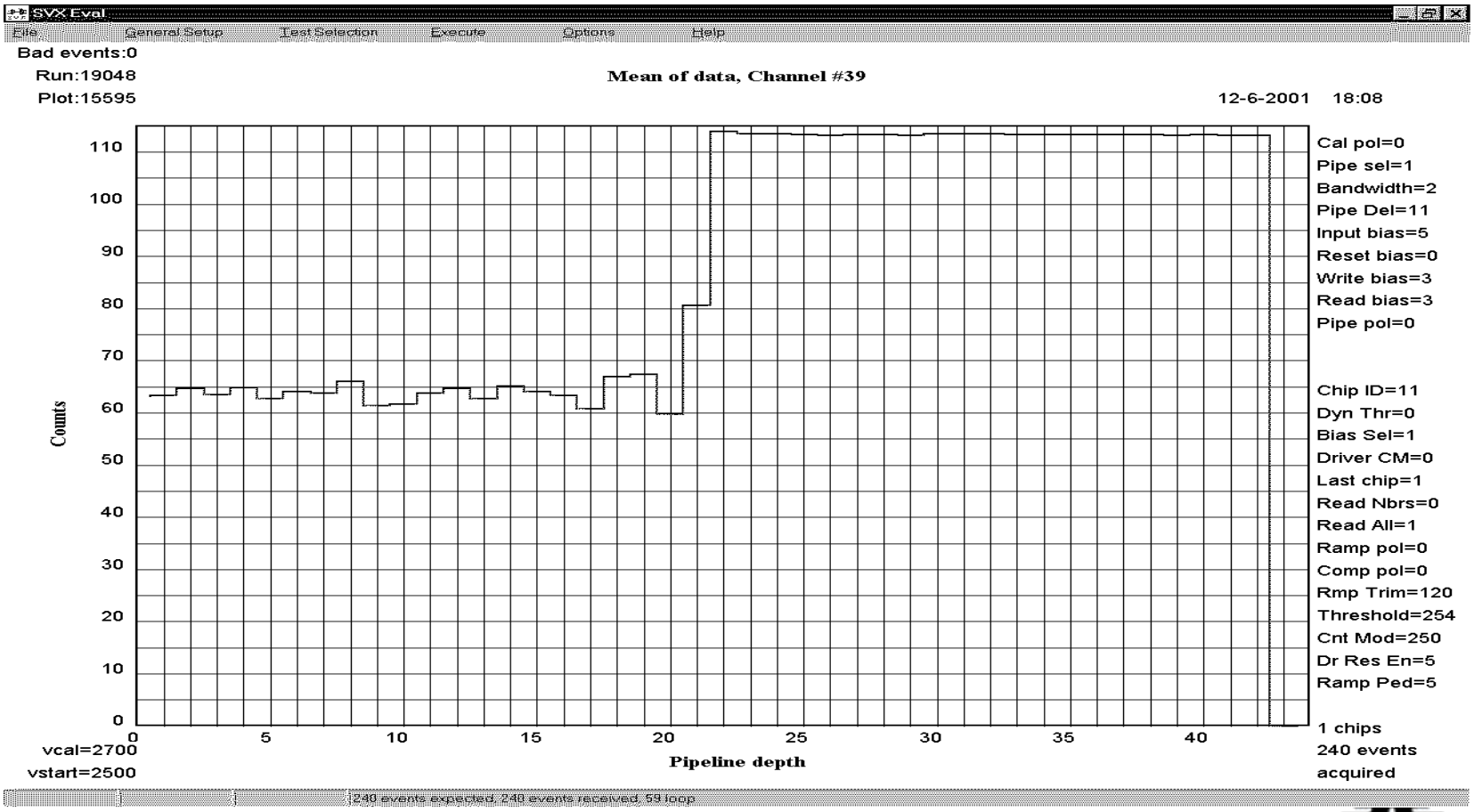
Cal Scan


SVX Eval
File
General Setup
Test Selection
Execute
Options
Help
Bad events:0
Run:19045
Plot:15592
Mean, Gain= 0.655 cnts/mV, Ped=-29.965 cnts, Channel #60
12-6-2001 17:25
Counts
Cal Inject / 2 mV
0
10
20
30
40
50
60
70
80
90
100
0
25
50
75
100
Cal pol=0
Pipe sel=1
Bandwidth=2
Pipe Del=11
Input bias=5
Reset bias=0
Write bias=3
Read bias=3
Pipe pol=0
Chip ID=11
Dyn Thr=0
Bias Sel=1
Driver CM=0
Last chip=1
Read Nbrs=0
Read All=1
Ramp pol=0
Comp pol=0
Rmp Trim=120
Threshold=254
Cnt Mod=250
Dr Res En=5
Ramp Ped=5
1 chips
240 events
acquired
vcal=2700
vstart=2500
240 events expected, 240 events received, 1 loop

L1A Delay Scan



Pipeline Depth



SVX

4 Status

- John Ledford (KU Engineer) will layout the SVX4 adapter card, we just need to request more time for him
- All technical documents and material available to him, we just have to work out the Finite State Machine – the stimulus machine output is multiplexed by the adapter card (Len and Petros?)