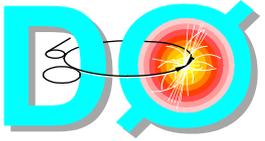
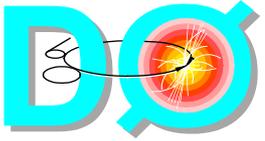


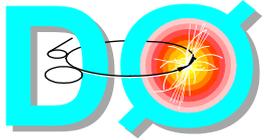
SVX4 Test Stand

Update and Progress Report

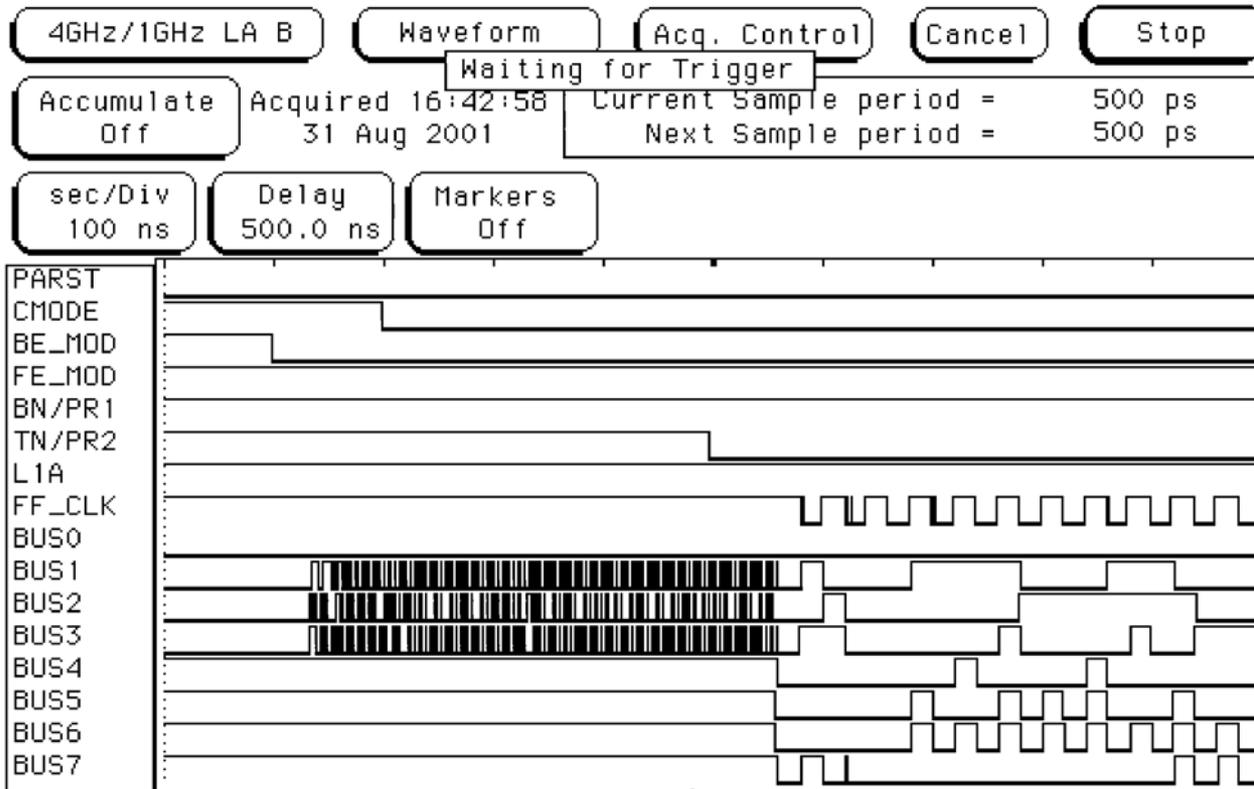


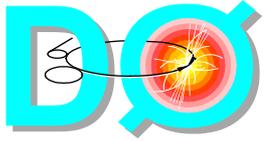
-
- We want to understand the SVX4 chip:
 - Exercise
 - Test
 - Characterize
 - Full (stimulus machine and logic analyzer) or minimal testing (PATT03)?





SVX3 Timing Diagram

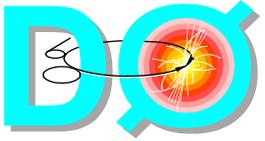




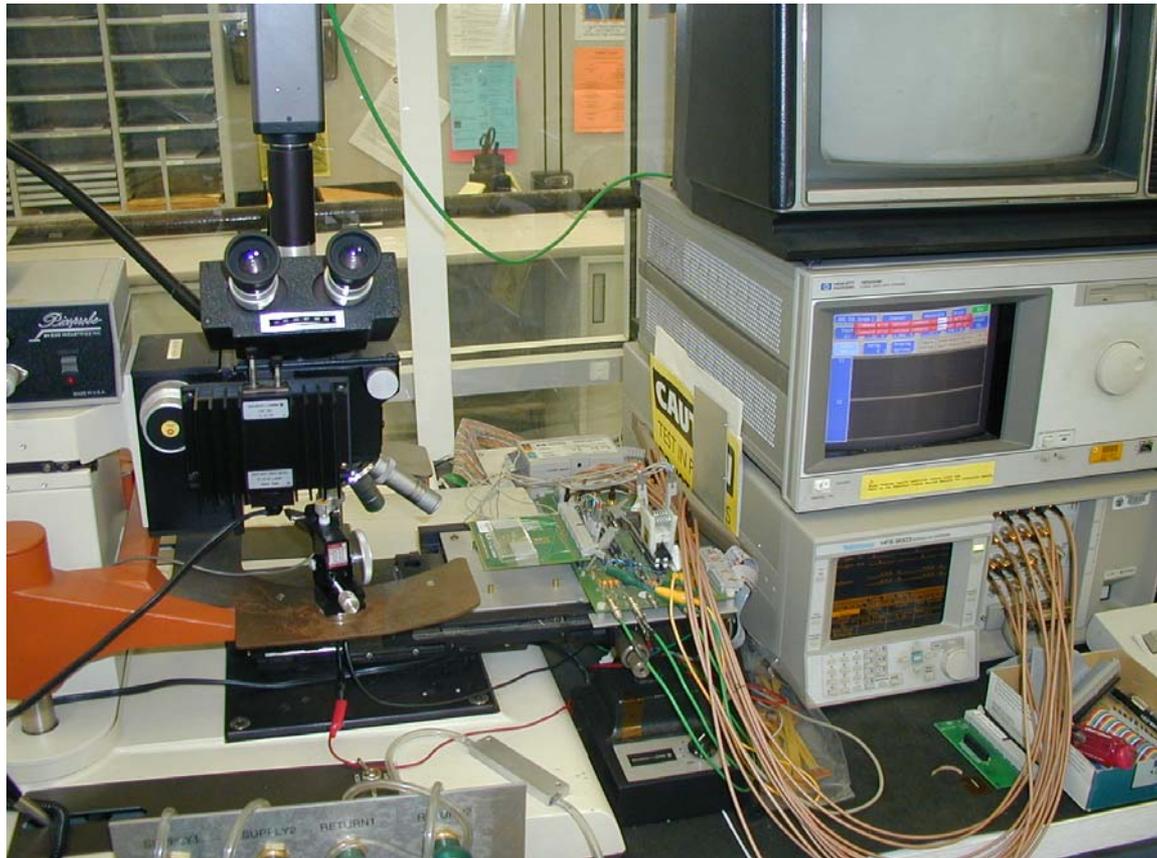
Projects in Progress



- We have been learning to use the logic analyzer and the stimulus system.
- We have been learning to use the CDF code that interfaces with the above two systems.
- Attempting to get a computer to use on the 14th floor of WH and untar the LBL SVX3 test suite.

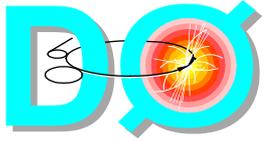


SVX3 Test Stand

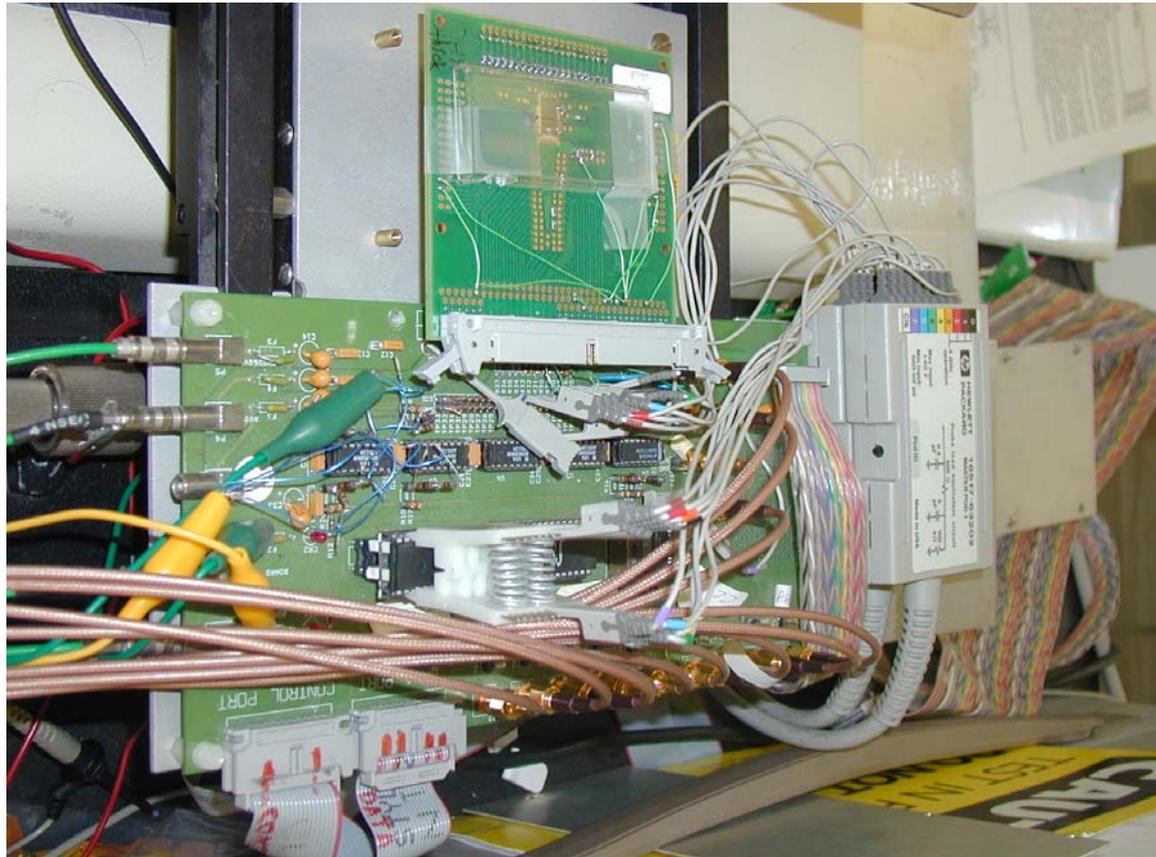


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SVX4 Electronics Meeting



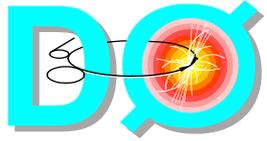
SVX3 Chip and Adapter



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SVX4 Electronics Meeting

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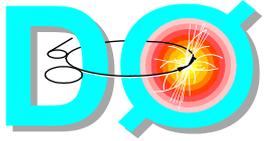
SVX4 Probe Card, PATT, and Interface Board



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SVX4 Electronics Meeting

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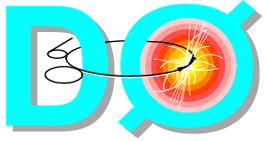


SVX4 Probe Station



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SVX4 Electronics Meeting



SVX4 Time Schedule



- Schedule from last CDF workshop:
 - Chip submission (End of November)
 - First parts back (Mid-January 2002)
 - First chips available (March 2002)
 - Full production order (June 2002)
- We must have tested and requested any modifications by June 2002!