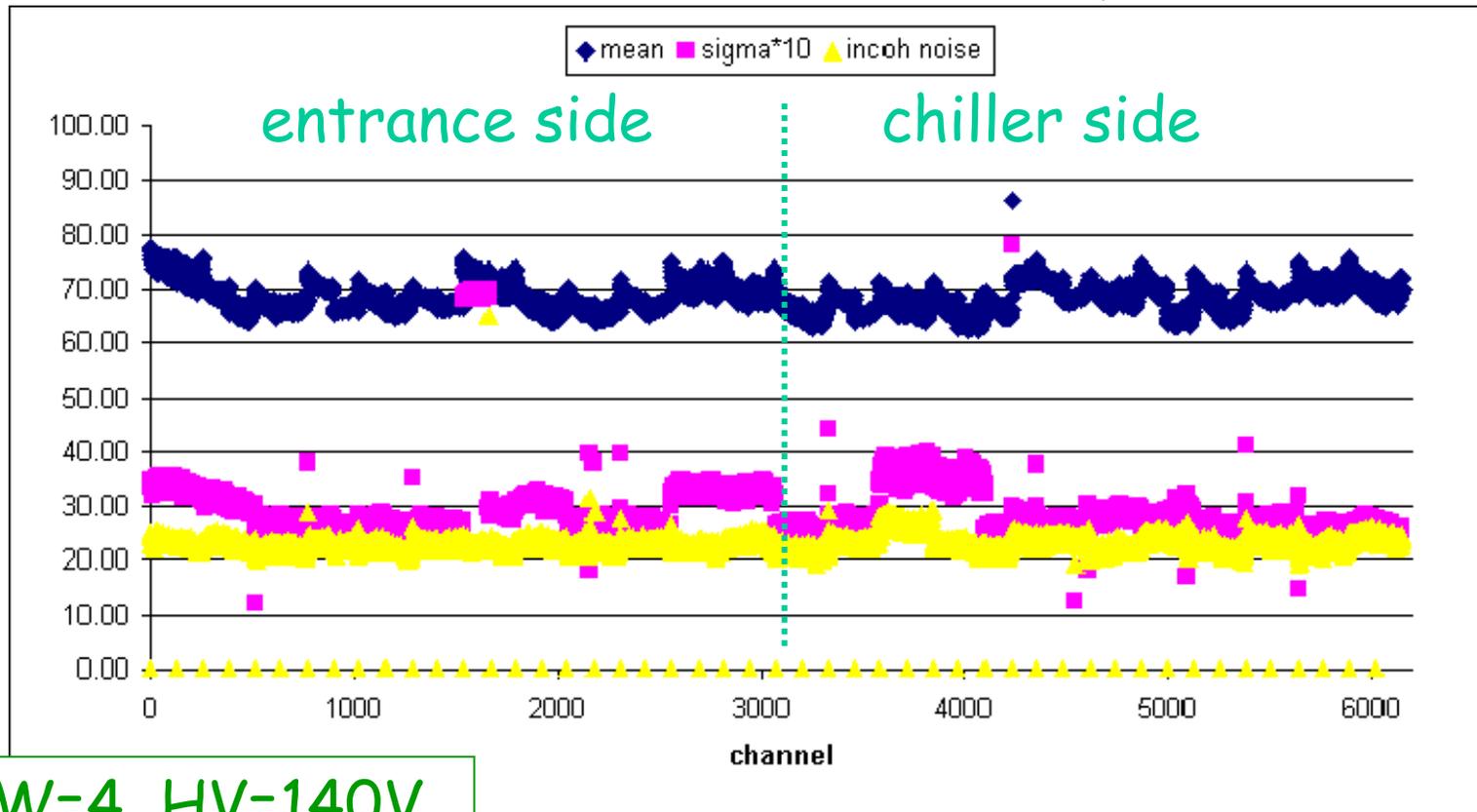


50% test (A-layer) summary

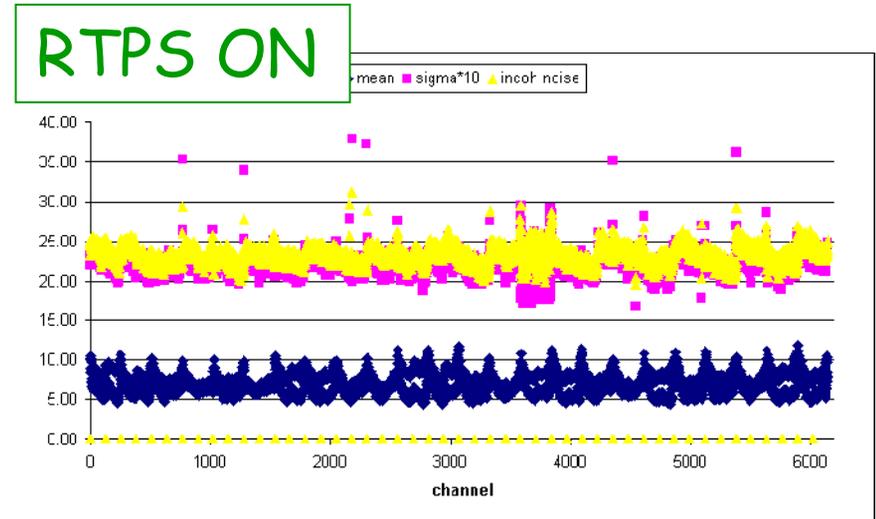
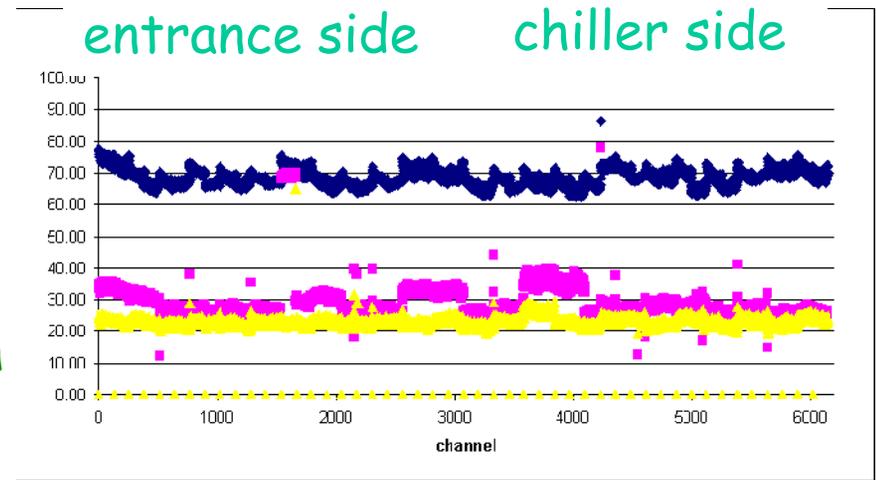
- Prepared and performed by many people

- All 24 modules were successfully read out



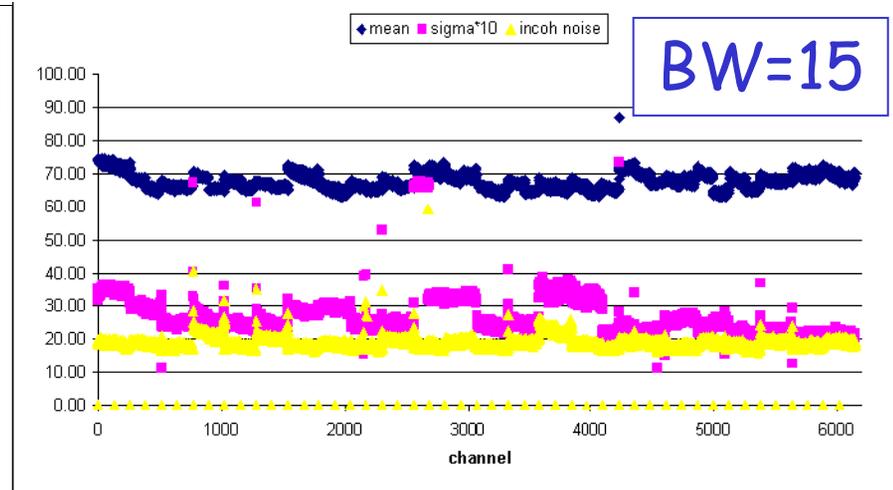
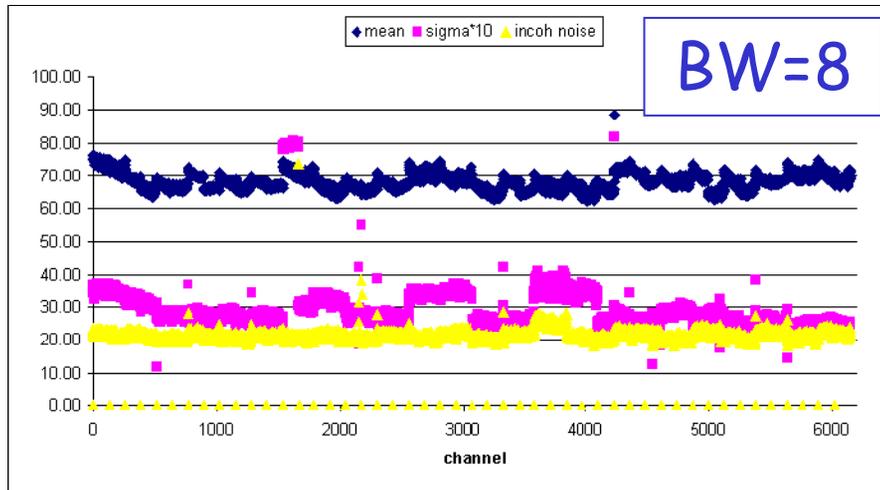
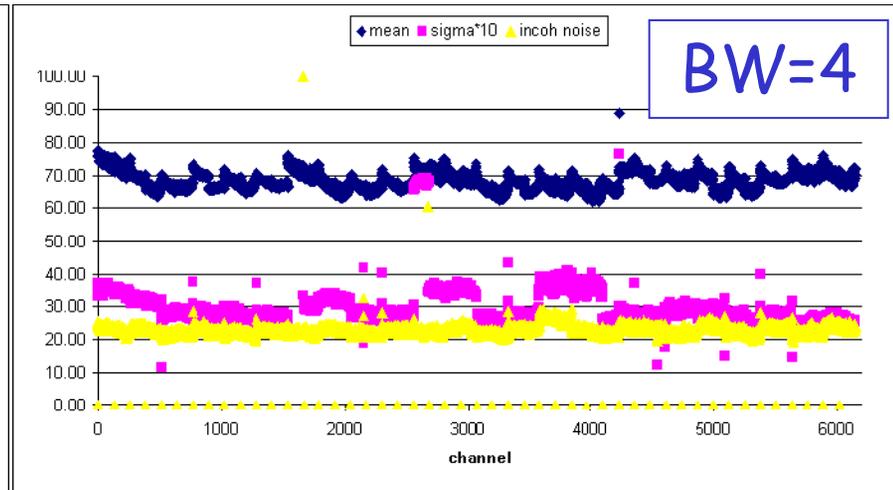
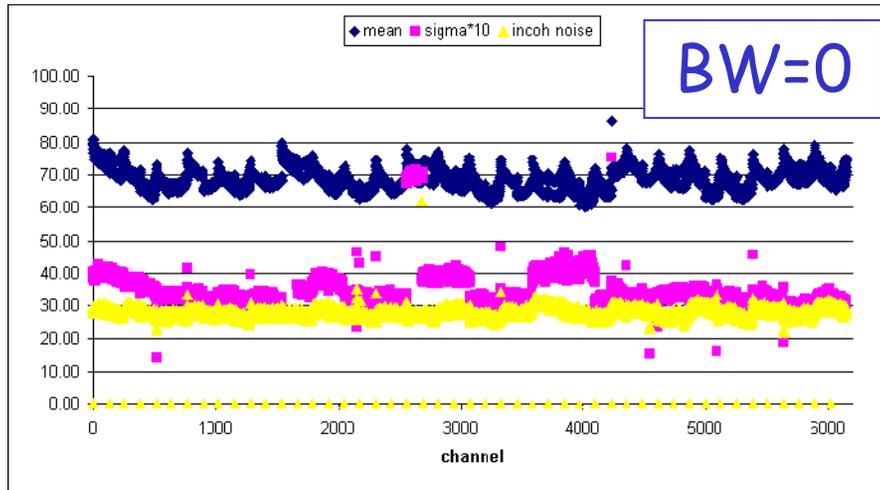
Noise

- Coherent noise slightly (<4 ADC counts) observed
 - module dependence
 - higher in the modules with the analog cable close to the support (?)
 - Entrance side slightly larger
 - Two SVX4 power supply, one for chiller and the other for entrance side
 - RTPS very strong (final) weapon
 - should not rely on it



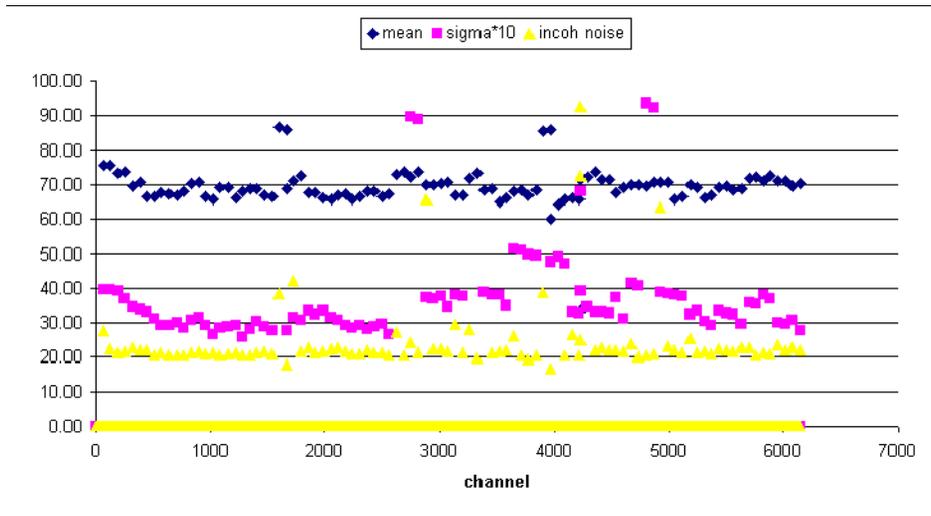
Bandwidth scan

HV=100V



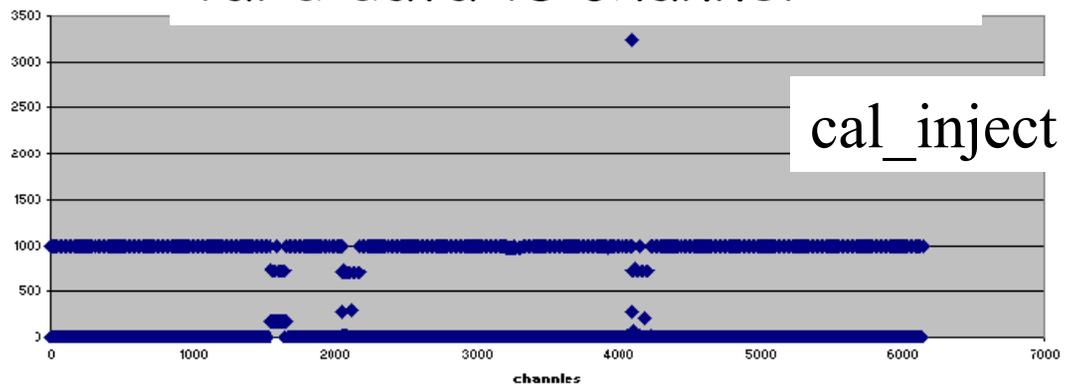
- Reasonable behavior (not only noise but also the pedestal uniformity)

Sparse readout



- Sparse readout works OK

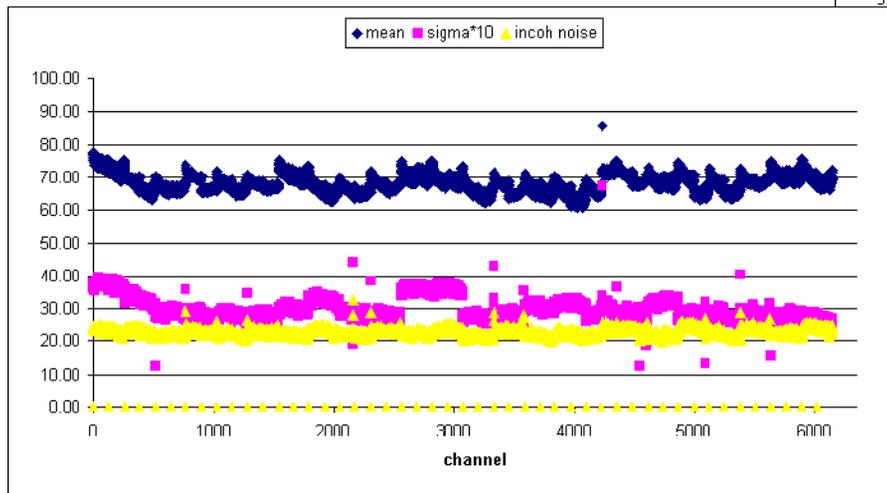
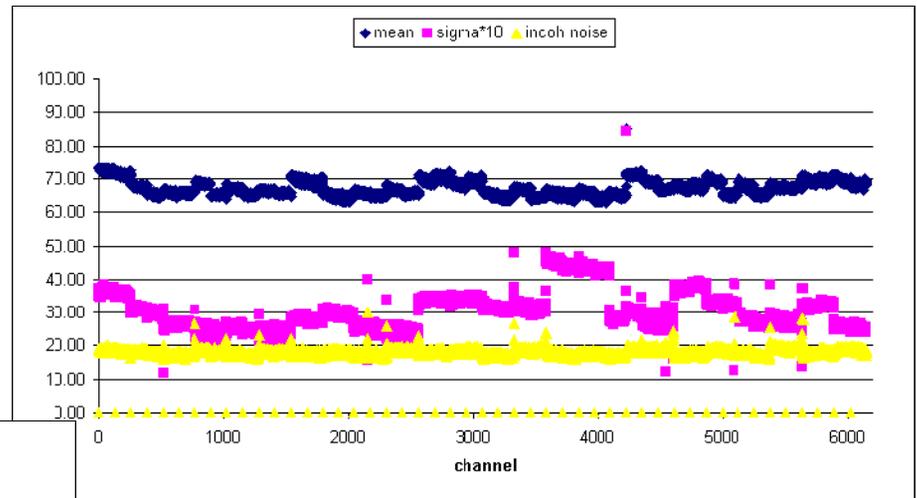
number of events with valid data vs channel



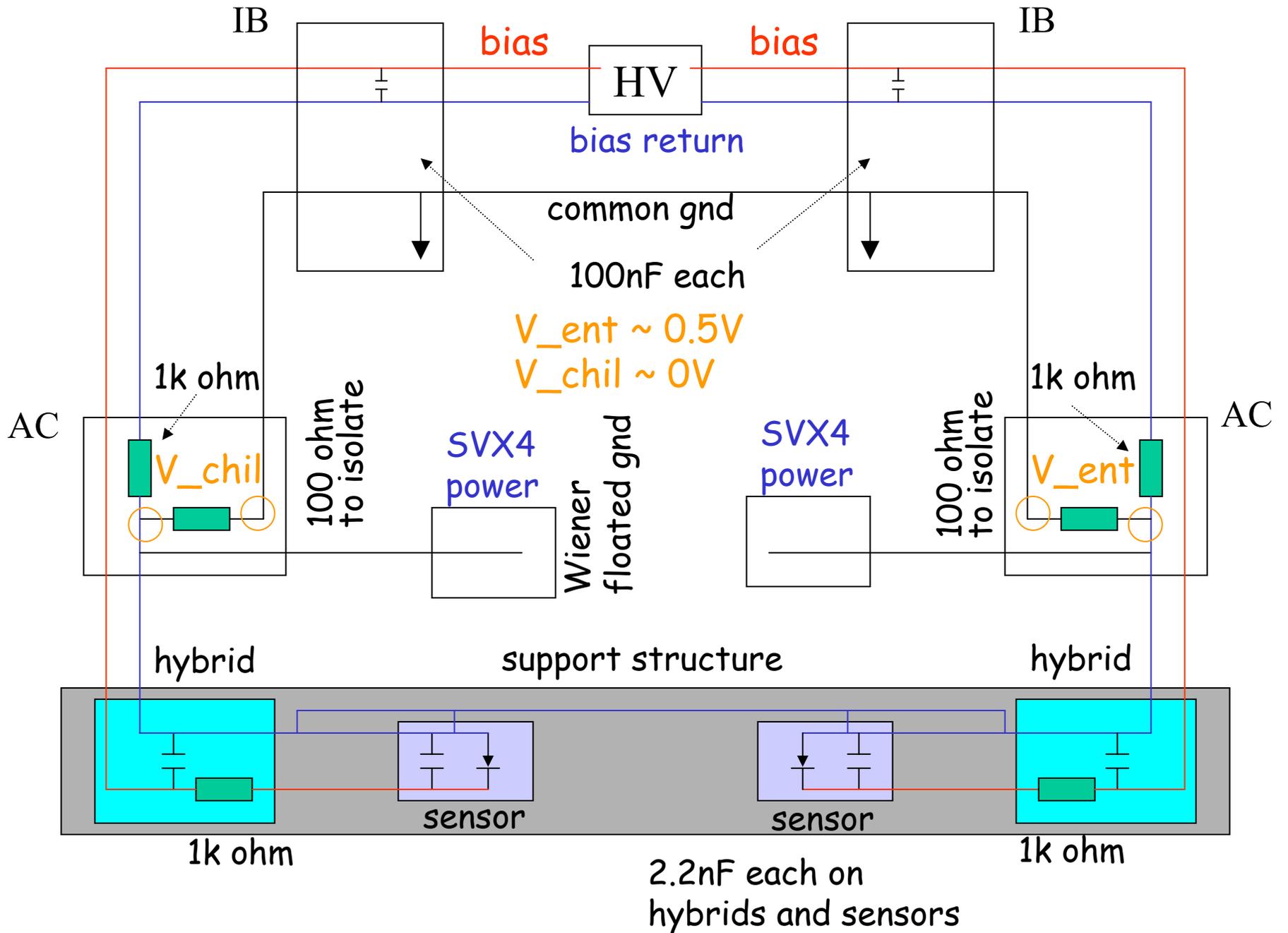
Trying to understand coherent noise

- Some efforts to understand the origin of the coherent noise → Not clear yet

power supply swapped



different ground scheme



Summary

- All 24 modules are alive !
- Succeeded to read out all of them simultaneously
- Noise level not perfect, bad not so bad
 - needs to understand the origin of the coherent noise → come back on the electrical testing setup
- BW scan, sparse readout, etc. were performed
- Sent data directly to Linux box from SBC
- Thanks to all involved