

Turn on procedure

- Make sure HV and SVX4 power off
- Before ramping up HV (module by module)
 - connect digital jumper cable
 - turn on SVX4 power, record the current
 - download, record the current
 - quick read out testing - 1k for pedestal and 1k for cal_inject
 - turn off SVX4 power (?) ---(*)
- After going through all modules
 - Cover the structure, make sure it light tight
 - (in case of (*), need to turn on SVX4 power and download)
 - turn on HV and ramp up to 50 V, make sure the current below 2 μA
 - ramp up HV to 200 V, I_{bias} should be $< 2 \mu\text{A}$

Test Menu

- Goal: Verify functionality for 24 modules and the noise level
- Short term - 10k each
 - pedestal
 - cal_inject
 - pedestal with sparsification
 - threshold 1, 2, 3, 5 sigma
 - repeat with RTPS on
- Long term -- ~ 8hours
 - count readout failure rate
 - monitor I_{bias} , DVDD, and AVDD current
- HV scan