

L1_Cal Int/Comm Team Toolbox

September 26 -September 30, 2005

Mon, Sept. 26	<p>TRANS:ATC front panel slot for adjacent connector not deep enough. Investigating cause and remedy.</p> <p>ADF->tape: Looking for possible bent pins on TAB backplane. Moving TAB to different slot. Runs planned to test 8's for each chip.</p> <p>Sim/Firmware comparison: Chad code matches sim/firm which truncates Et value.</p>
Tues, Sept. 27	See above.
Wed, Sept. 28	<p>ADF->tape: Intermittent 8's due to bent pin on TAB bkpln for chip 2. Wrong BC in TAB L2 buffer due to SCL/VME configuration (timing). Testing versions of TAB global firmware. Jaro investigating eta #ing scheme.</p> <p>Algo/sim: Cory code calculates Et without truncating-firmware/sim/Chad code truncates. Determining if truncation causes overflow.</p> <p>Scheduling meeting @ 1.</p>
Thurs, Sept. 29	<p>Algo/Sim: truncation causes overflow. Result sent to Jaro.</p> <p>Schedule update-see weblink</p> <p>Wiener operating parameters for EPICS being generated.</p>
Fri, Sept. 30	D0 Collaboration Plenary