

L1_Cal Int/Comm Team Toolbox

October 24 –October 28, 2005

<p>Mon, Oct. 24</p>	<p>GAB→TFW: Timed in—ready for latency measurement. TAB:Jaro working on eta/phi mapping scheme in SW chips TAB→L2:Ready for VME/SCL masks. ALGO: TT convention change needed for e ID INFRA:Preparing to install additional ADF rack and Control crate.</p>
<p>Alan Cory Lyn Mike</p>	
<p>Tues, Oct. 25</p>	<p>INFRA:Test stand is down for installation tasks. Racks repositioned for new ADF rack. TRANS:40 PP arrived. TAB: eta/phi firmware update available. TAB/GAB→L2/L3:Header format clean-up underway.</p>
<p>Alan Lyn Mike Sabine</p>	
<p>Wed, Oct. 26</p>	<p>INFRA:Test stand is down for installation tasks. Dual ADF crate installed. Control crate will be installed today. Quest for TAB/GAB backplane hardware underway.</p>
<p>Alan John F. Lyn Mike Sabine</p>	
<p>Thurs, Oct. 27</p>	<p>TCC connected as an online Linux node. Testing communication between TCC/TAB/Readout today. TAB/GAB backplane hardware drawings found. Getting bars manufactured.</p>
<p>Alan Dan Lyn Philippe</p>	
<p>Fri, Oct. 28</p>	<p>TCC→Control crate complete. TCC→ADF via VI complete. TCC→TAB/GAB in progress. TCC→Readout in progress. ADF timing adjustment of 17 rf buckets implemented today. Operations: Bagby on deck.</p>
<p>Dan Lyn Mike Philippe</p>	