

L1_Cal Int/Comm Team Toolbox

July 18-22, 2005

Mon, Jul 18	<p>Week's tasks: Installation week -Transition system/LVDS/PFC GAB→L3: Measure latency Bxing sync w/hand pulser TAB→L3: Bxing sync w/hand pulser Compare precision readout w/L1cal.</p>
Tues, Jul 19	<p>TAB-->L2/L3 (Mulhearn/Anderson) Found that G-link error indication on HP receiver is not asserted. Confirmed that protocol is correct to FIC. James (L2) looking at FIC to determine how error indication is generated. Investigating VTM/VRB error indications. GAB-->TFW (Cory/Mike) Verified an AND/OR term generated on Bxing7 w/trig on sync gap. The Bxing check capability was disabled. Now testing with check enabled. SCL-->TAB/GAB (Mike) Sending Nevis SCL/VME firmware used at Fermilab to be updated. Established data/Bxing # in data stream w/trig file using Cal pulser system. Can run between stores. TRANS SYS (Fogie) Planning to prepare two ADF racks with cable routing hardware tomorrow morning. Jorge found a bad (1 channel) patch panel card while testing 2 PPC/PFC/ATC combo units. Requested a locker and boxes for tested units. ORC (Bagby/Charlotte) scheduled to meet tomorrow.</p>
Wed, Jul 20	<p>TAB-->L2/L3 (Mulhearn/Anderson) Identified systematic FIC error on ch0. James is addressing this issue. Utilized VTM ch1 for tests. See Mike's e-mail for details. Ran 5 million events to L2 w/36 events containing 0 data--NO sync loss, word, or protocol errors. Sending data to Jaro for consultation. GAB-->L2 (Mulhearn) Transmission test scheduled for this week. TRANS SYS (Foglesong/Rahmi) Test card in house. Expected to cycle through assembly today. Cable routing hardware installation today.</p>
Thurs, Jul 21	<p>TAB→L2/L3(Mulhearn) Establishing latest firmware updates on TAB/GAB/SCL/VME components for sync studies. Hal provided a web link to keep them in order. Mike believes the zero data issue may be resolved with the sync update in firmware. BER parameter will be stated AFTER firmware is in order. The issue Anderson raised about studying the drop out of 'online' mode has been resolved. Mike reported it was a software driver default setting on the SCL/VME that occurred whenever a reset was invoked. The driver has been fixed. Anderson's contribution, for now, is completed. TAB-->L2 Sent an email to Mikolaj and James to attend tomorrow's meeting to organize their work. GAB-->TFW (Cory) Understanding the GUI. TRANS SYS (Fogie/Martinez) Installing hardware today. GlenAir test card still in assembly group. They had a problem installing a connector. ORC (Charlotte/Lyn) Extracting basic ORC required line drawings from available files for AC distribution. DC distribution is next.</p>
Fri, Jul 22	<p>TAB-->L2/L3 (Mulhearn) Generated block diagram of timing scheme. Discussing offline w/Hal on how to proceed. TAB-->L2 (Mikolaj) Running long term (hrs) to FIC on single channels. Fibers for splitter tests ordered. GAB-->TFW (Mulhearn/Cory) Latest firmware downloaded. Requested that all files should be sent when updating firmware. ORC (Charlotte/Lyn) AC line dwg will be finished today. DC in progress.</p>