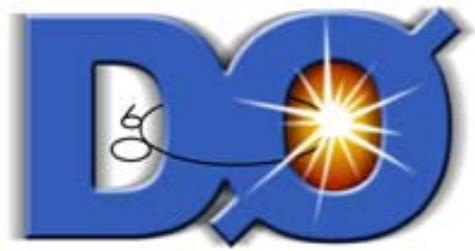


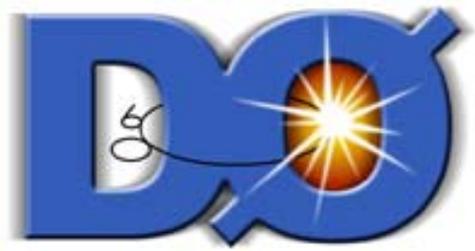
Run2b SMT 1% Teststand

- ✓ Readout errors in VRBC & 1553 modes are understood (Mike's presentation at the last meeting)
- ✓ SBC with relatively old software is working
- ✓ Several data files were written from SBC to Linux computer

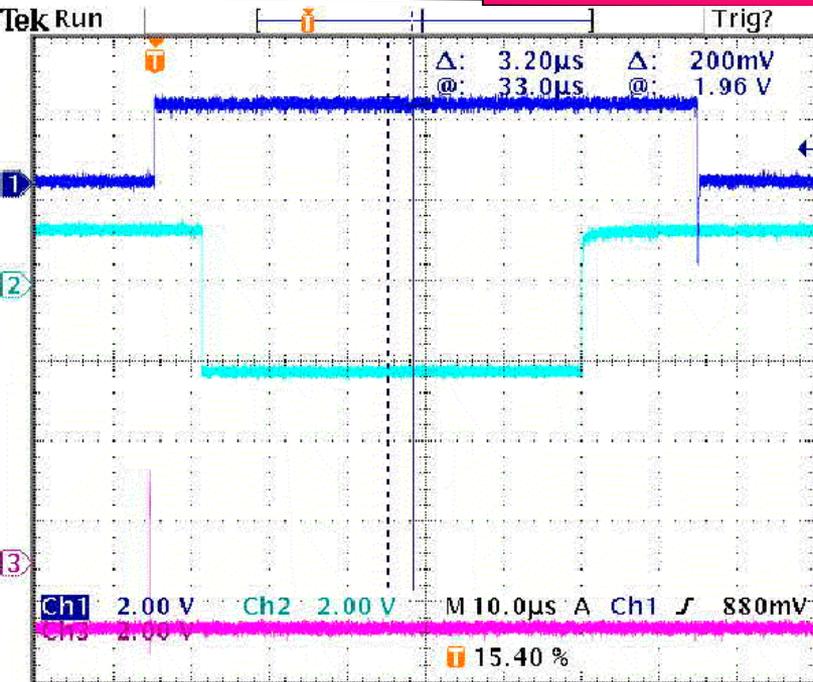
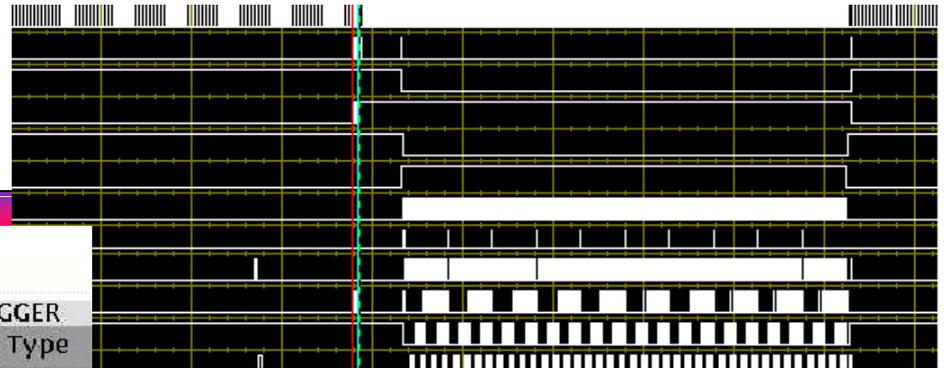


SCL Mode

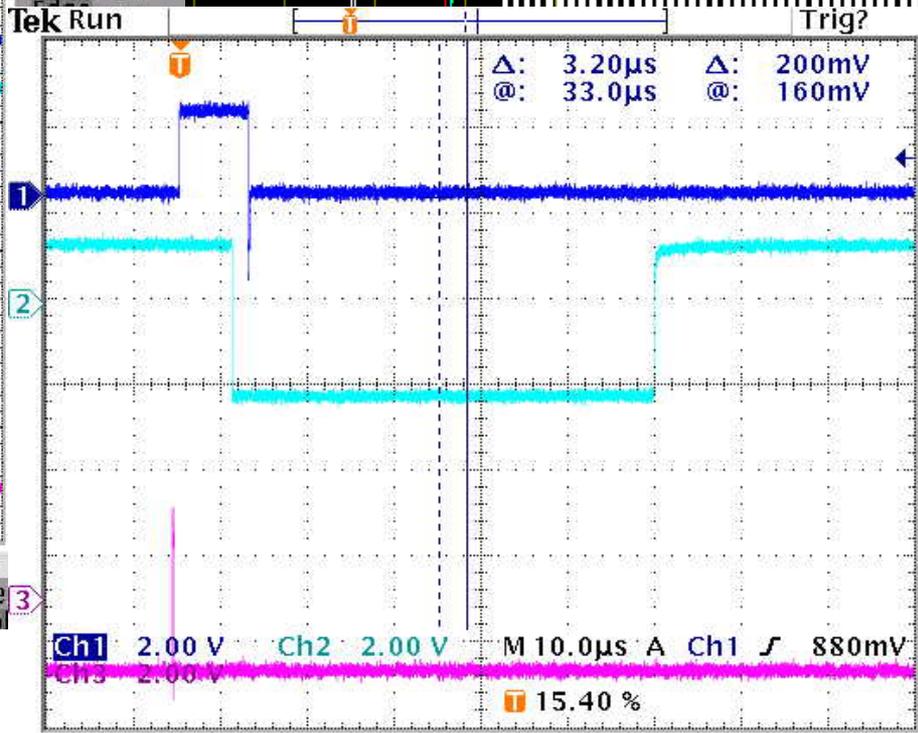
- ✓ SCL receivers were modified (Ted Zmuda)
- ✓ New SCL firmware was downloaded (there are clocks during acquire)
- ✓ Front End Busy signal is short for every second event (with some exceptions)
 - Readout for these events is incomplete
 - Logic Analyzer & Data Available show that the readout from hybrid is OK
 - VRBC firmware is suspected



Short FEB



TRIGGER
A Trig Type



TRIGGER
A Trig Type

Edge
Video

Mode
Auto
Normal

Source
Ch: 1 2 3 4
Alt
Ext Ext/10
AC Line

Coupling
DC Noise
HF Reject
LF Reject

Slope
↗ ↘

VERTICAL (CH1)
Coupling Impedance Bandwidth
DC AC \neq 1M Ω 50 Ω Full
ACQUIRE
Fast Trig Mode
Normal Sample

VERTICAL (CH1)
Coupling Impedance Bandwidth
DC AC \neq 1M Ω 50 Ω Full
ACQUIRE
Fast Trig Mode
Normal Sample
CURSOR
Off
HBar VBar