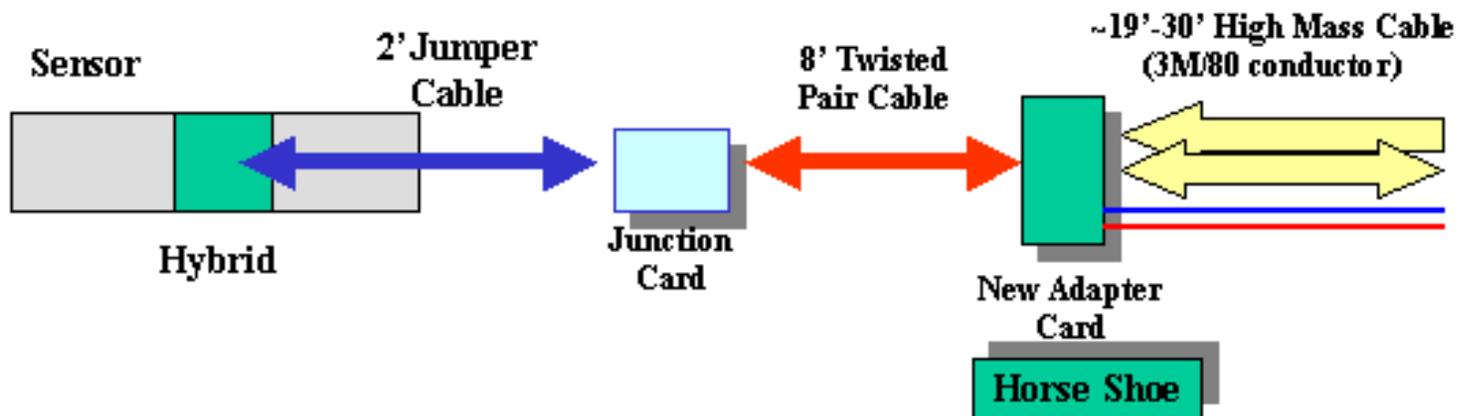




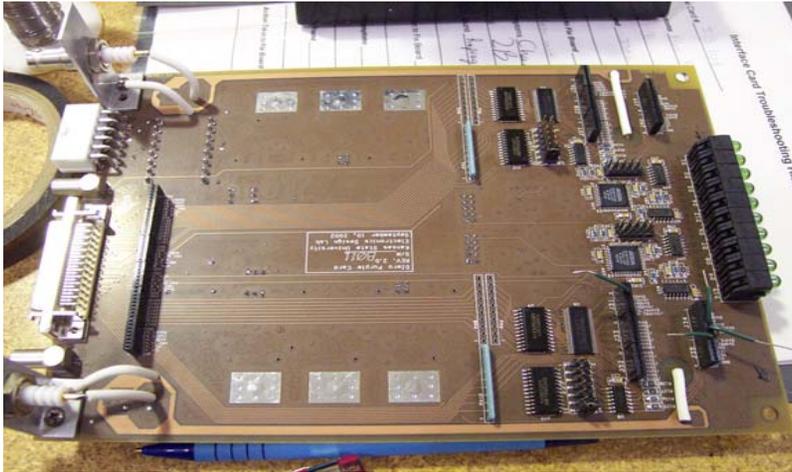
Electronics: Junction Cards, Adapter Card, Purple Card,

Ron Sidwell, K. Harder, T. Sobering, N. Stanton, R. Taylor, E. VonToerne, *Kansas State U.*





Purple Card

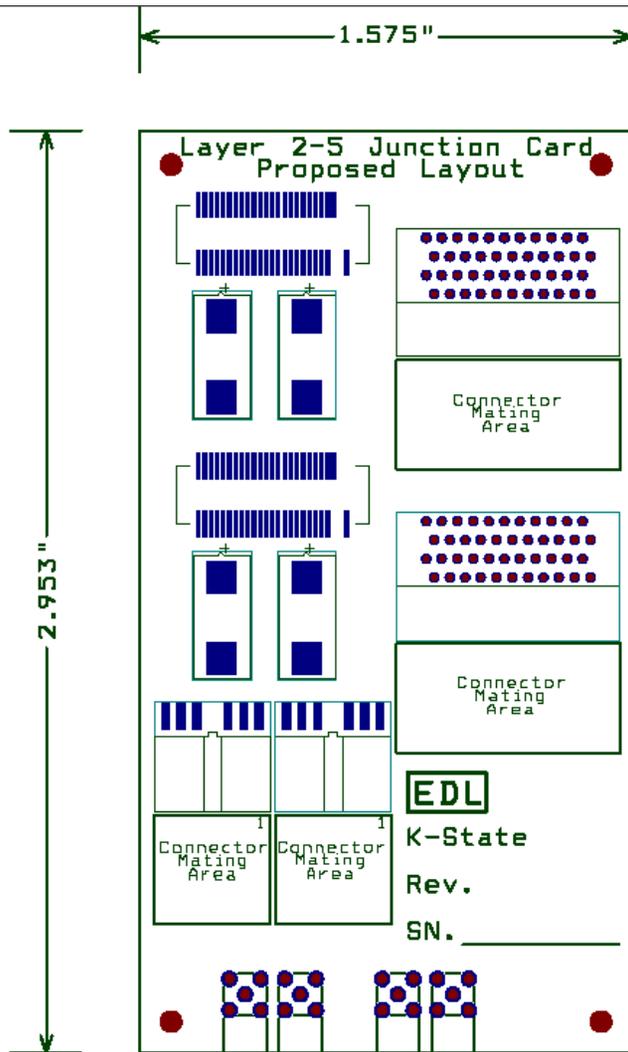


Communicates between Standalone sequencer and hybrid. 50-conductor input cable, digital jumper cable output. Voltage regulation, signal translation, temperature monitoring, etc., etc.

- Rev 2, twelve built. No design changes pending. But two failures of capacitor C42 recently- **will pretest caps before install, and add “ramp” on first turnon as suggested by the vendor.**
- Four more being built from remaining PCBs.
- Readiness review? Eckhard –
- **Do we need to add over-voltage protection on Vcal?**



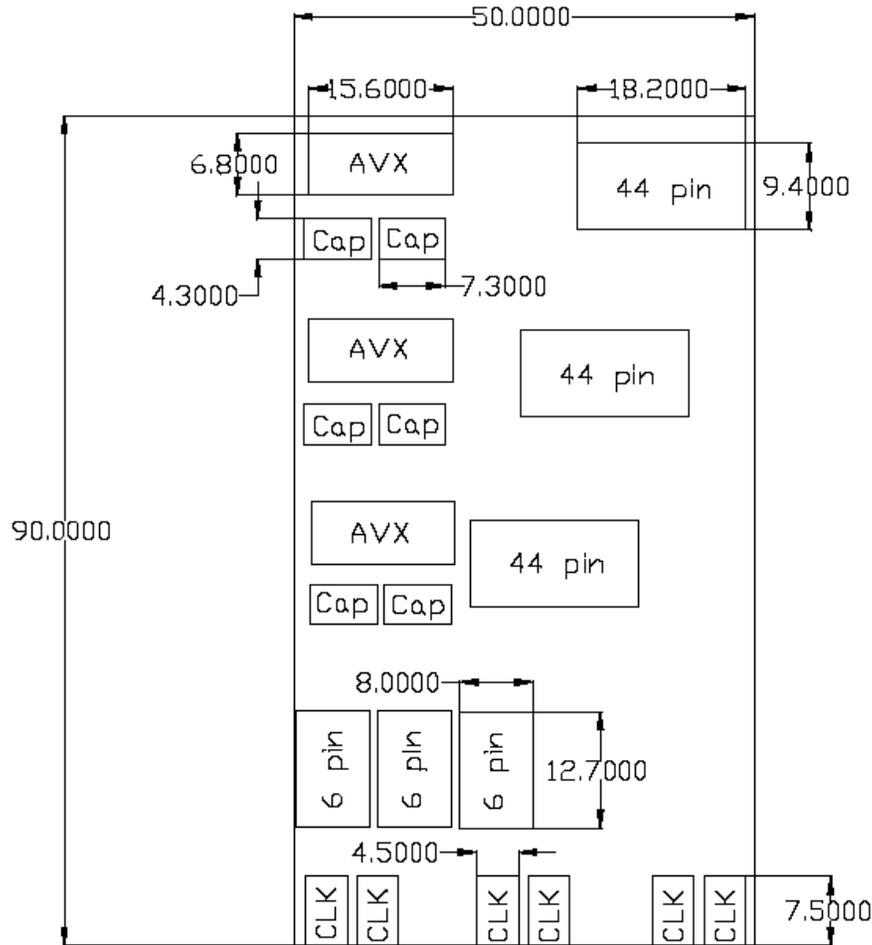
Proposed L2-L5 Junction Card Layout



- Using connectors has cost implications that need to be addressed (>\$50K). Connectors are the same as used on the adapter card.
- Now building 3 mockups (connectors only).



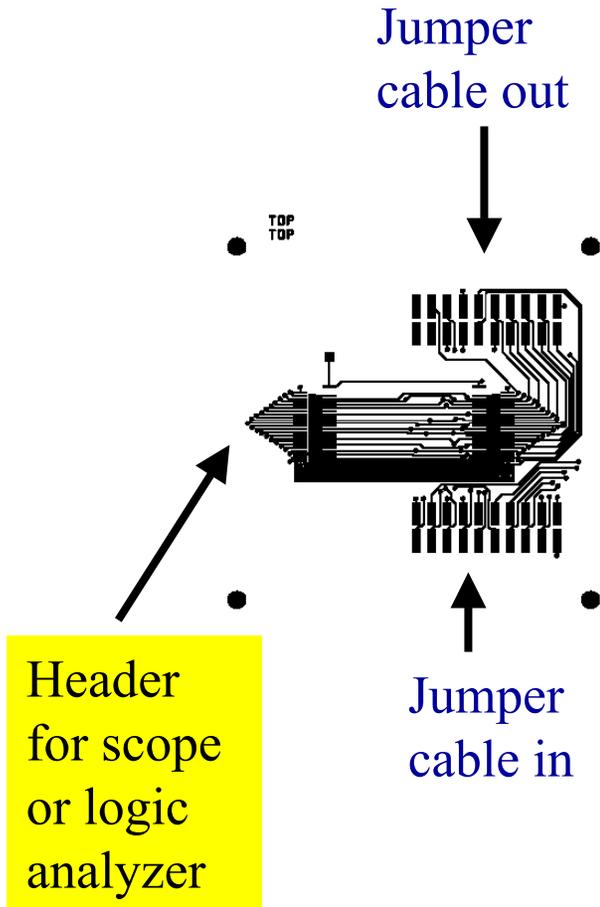
L0-L1 junction card conceptual design



- Connector layouts include mating connector. Connectors are same as adapter card, except for the clocks, which are edge-mounted (bisected by the PCB).
- Rough cost/channel:
 - \$36 Omnetics connector
 - \$8 power
 - \$10 two clocks
 - Plus connectors on cables(\$~50 channel)
 - Plus new prototype cost (not in schedule!)



Russell's Breakout card (diagnostics)



- Allows one to spy on signals near, but not on, the hybrid (its safer that way).
- Now ready. Brought two to Fermilab.



Other News

Four more adapter cards are being stuffed. Should be ready in around 10 days, after the purple cards get finished.

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Russell working on component layout on next revision adapter card. Space is very, very tight as expected. Can we get back some depth??