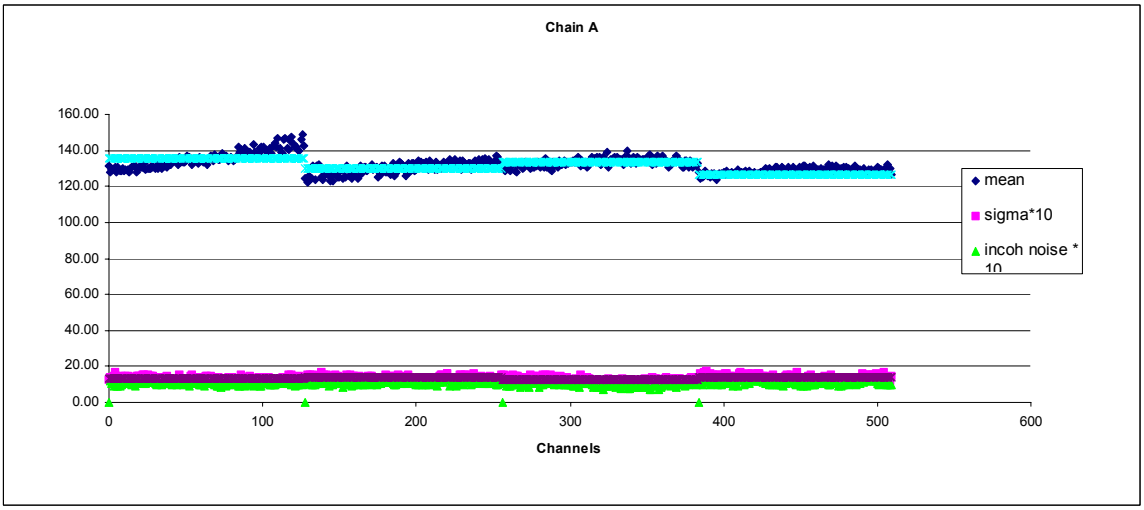
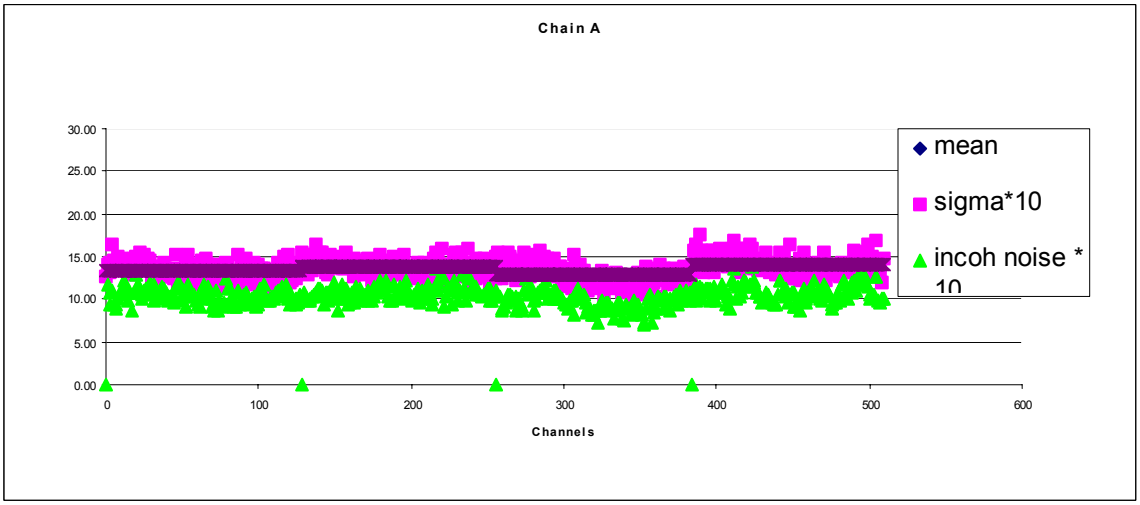
9/12/2002 hybrid #13 SVX4 V_1 BW=4





Read out chain 1	**ERRORS =**	**0**					
p side		chip ID:	E9	E8	E7	E6	
PABW (4bits)	40100						
Ramp Trim (3)	010	2					
Preamp Cur (4)	40100		**Errors**	**String**	**1**		
Pipe W Cur (2)	101		**first**	**second**	**third**	**fourth**	
Pipe R Cur (2)	101		**0**	**0**	**0**	**0**	
Pipeline (6)	8001000						
Driver I (3)	6110						
Threshold (8)	2000100000						
MaxCount (8)	8010000000						
Chip ID (7)	E011100000	id	11101001	11101000	11100111	11100110	
ADC Ped. (4)		ped	8	8	8	8	
POL, 0=R_up (1)	0	polarity	0	0	0	0	
0=Cal Mask (1)	0						
0=PedFirst (1)	0						

0=RTPS_on (1)	1
1=Read 127 (1)	0
1=Read 63 (1)	0
1=Read All (1)	1
1=Read Neigh (1)	0