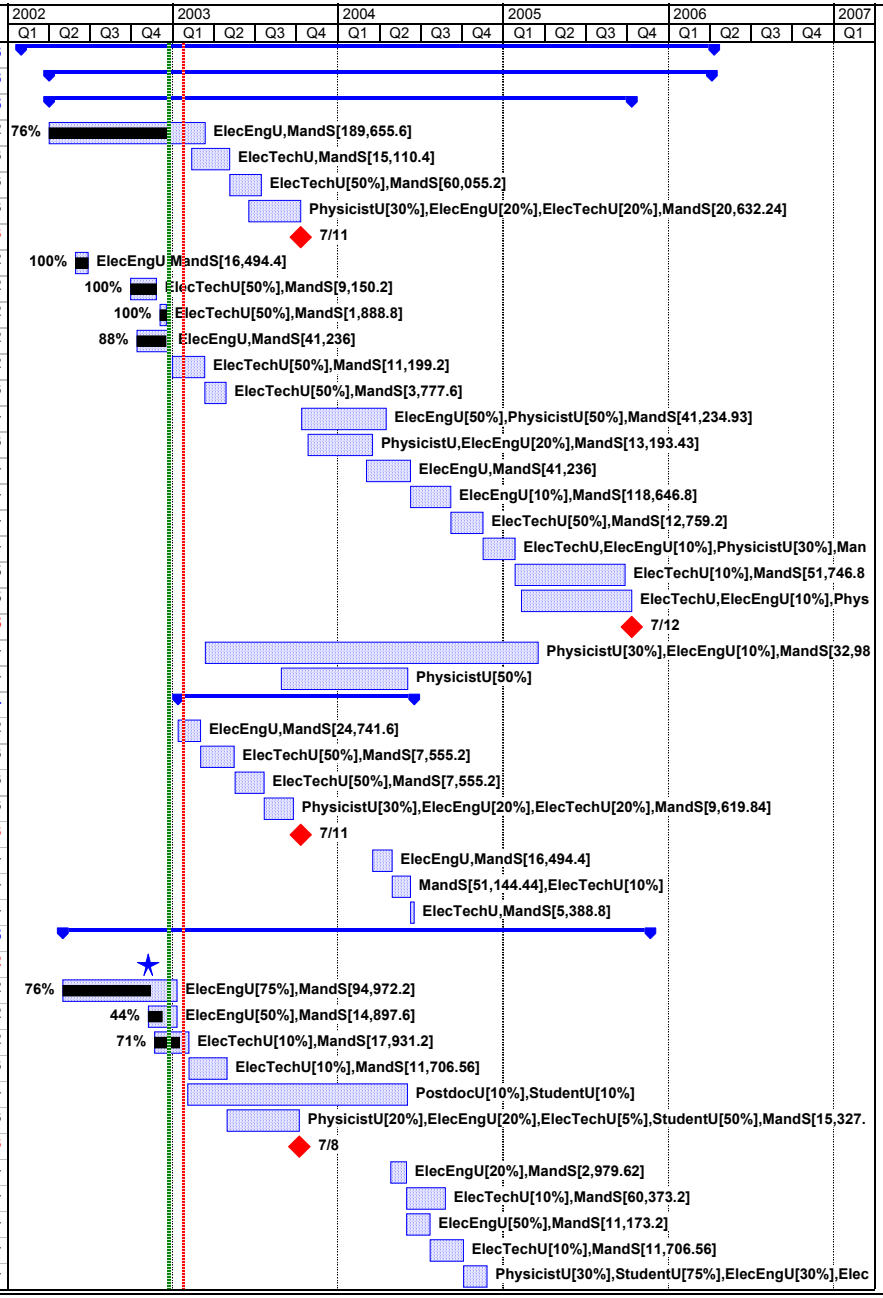
Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
1	1.2	Run IIb Trigger Upgrade	206.8 w	11/1/01	1/10/06
2	1.2.1	Level 1 Calorimeter Trigger	199.2 w	1/2/02	1/5/06
3	1.2.1.1	ADC/Digital Filter (ADF)	175.8 w	1/2/02	7/12/05
4	1.2.1.1.1	Design prototype ADF	48.4 w	1/2/02	12/12/02
5	1.2.1.1.2	Layout prototype ADF	10 w	11/13/02	2/4/03
6	1.2.1.1.3	Fabricate/Assemble Prototype ADF	10 w	2/5/03	4/15/03
7	1.2.1.1.4	Bench Test Prototype ADF	16.2 w	3/19/03	7/11/03
8	1.2.1.1.5	Prototype L1 Calorimeter Trigger ADF Shipped To Fermilab	0 w	7/11/03	7/11/03
9	1.2.1.1.6	Design analog splitter	4 w	3/1/02	3/28/02
10	1.2.1.1.7	Layout and fabricate analog splitter	8 w	7/1/02	8/26/02
11	1.2.1.1.8	Bench test analog splitter	2 w	9/4/02	9/17/02
12	1.2.1.1.9	Design SCL interface	10.2 w	7/15/02	9/24/02
13	1.2.1.1.10	Layout and fabricate SCL interface	10 w	10/1/02	12/11/02
14	1.2.1.1.11	Bench test SCL interface	5 w	12/12/02	1/27/03
15	1.2.1.1.12	Develop in-situ exercizing/debugging software	25 w	7/14/03	1/16/04
16	1.2.1.1.13	ADF Prototype in-situ tests	20 w	7/28/03	12/17/03
17	1.2.1.1.14	Final ADF design - Production readiness review	12.4 w	12/4/03	3/10/04
18	1.2.1.1.15	Procure ADF components	12.4 w	3/11/04	6/7/04
19	1.2.1.1.16	Fabricate, Assemble 10% ADF	10 w	6/8/04	8/17/04
20	1.2.1.1.17	Bench test 10% ADF	10 w	8/18/04	10/27/04
21	1.2.1.1.18	Fabricate production ADF	32.4 w	10/28/04	6/27/05
22	1.2.1.1.19	Bench test production ADF	32.4 w	11/11/04	7/12/05
23	1.2.1.1.20	L1 Calorimeter Trigger ADF Production Complete And Tested	0 w	7/12/05	7/12/05
24	1.2.1.1.21	Digital filter FPGA programming	100 w	12/13/02	12/17/04
25	1.2.1.1.22	ADF offline software and analysis	37.6 w	5/30/03	3/4/04
26	1.2.1.2	ADF Crates	70.2 w	10/14/02	3/18/04
27	1.2.1.2.1	Design transition backplane	6.8 w	10/14/02	12/2/02
28	1.2.1.2.2	Layout and fabricate transition backplane	9.2 w	12/3/02	2/14/03
29	1.2.1.2.3	Fabricate/Assemble Prototype crate	9.2 w	2/17/03	4/21/03
30	1.2.1.2.4	Test prototype crate ADF in prototype crate	9.2 w	4/22/03	6/25/03
31	1.2.1.2.5	L1 Calorimeter Trigger ADF+Crate Prototype Complete	0 w	7/11/03	7/11/03
32	1.2.1.2.6	Final backplane design	4.6 w	12/18/03	1/29/04
33	1.2.1.2.7	Procure crate components	5.8 w	1/30/04	3/10/04
34	1.2.1.2.8	Assemble ADF crates	1.2 w	3/11/04	3/18/04
35	1.2.1.3	Trigger Algorithm Board	177.2 w	2/1/02	8/22/05
36	1.2.1.3.1	Choose L1 Calorimeter Trigger Baseline Jet Algorithm	0 w	8/8/02	8/8/02
37	1.2.1.3.2	Design prototype Trigger Algorithm Board (TAB)	35.6 w	2/1/02	10/11/02
38	1.2.1.3.3	Layout prototype TAB	9 w	8/9/02	10/11/02
39	1.2.1.3.4	Procure prototype TAB parts	10.6 w	8/23/02	11/6/02
40	1.2.1.3.5	Fabricate/Assemble Prototype TAB	10 w	11/7/02	1/29/03
41	1.2.1.3.6	Develop TAB/GAB test software	65 w	11/4/02	3/3/04
42	1.2.1.3.7	Test prototype TAB	22.4 w	1/30/03	7/8/03
43	1.2.1.3.8	L1 Calorimeter Trigger TAB Prototype Complete	0 w	7/8/03	7/8/03
44	1.2.1.3.9	Pre-production TAB design	5 w	1/27/04	3/1/04
45	1.2.1.3.10	Procure TAB parts	12.4 w	3/2/04	5/26/04
46	1.2.1.3.11	Layout pre-production TAB	7.6 w	3/2/04	4/22/04
47	1.2.1.3.12	Fabricate/Assemple pre-production TAB	10 w	4/23/04	7/5/04
48	1.2.1.3.13	Bench test pre-production TAB	7.6 w	7/6/04	8/26/04



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Task | Split | Milestone | Completed Milestone
Critical Task | Progress | Critical Milestone | Summary

Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
49	1.2.1.3.14	Final design and layout changes to TAB	2.4 w	2/9/05	2/24/05
50	1.2.1.3.15	Fabricate production TAB	10 w	2/25/05	5/5/05
51	1.2.1.3.16	Bench test production TAB	15 w	5/6/05	8/22/05
52	**1.2.1.4**	**Global Algorithm Board (GAB)**	**170 w**	**7/29/02**	**1/5/06**
53	1.2.1.4.1	Design prototype GAB	19 w	7/29/02	12/11/02
54	1.2.1.4.2	Layout prototype GAB	5 w	12/12/02	1/27/03
55	1.2.1.4.3	Procure prototype GAB parts	12.4 w	1/6/03	4/2/03
56	1.2.1.4.4	Fabricate/Assemble Prototype GAB	5 w	4/3/03	5/7/03
57	1.2.1.4.5	Test Prototype GAB	20 w	5/8/03	9/29/03
58	**1.2.1.4.6**	**L1 Calorimeter Trigger GAB Prototype Complete**	**0 w**	**9/29/03**	**9/29/03**
59	1.2.1.4.7	Final GAB design - Production readiness review	5 w	1/27/04	3/1/04
60	1.2.1.4.8	Procure GAB parts	12.4 w	3/2/04	5/26/04
61	1.2.1.4.9	Layout final GAB	7.6 w	3/2/04	4/22/04
62	1.2.1.4.10	Fabricate production GAB	10 w	8/23/05	11/1/05
63	1.2.1.4.11	Bench test production GAB	7.6 w	11/2/05	1/5/06
64	**1.2.1.5**	**Cables**	**148 w**	**9/9/02**	**8/31/05**
65	1.2.1.5.1	Design and Fabricate system to test ADF to TAB cables	6.2 w	9/9/02	10/21/02
66	1.2.1.5.2	Test ADF to TAB cables	2.4 w	10/22/02	11/6/02
67	1.2.1.5.3	Procure ADF to TAB cables	12.4 w	6/6/05	8/31/05
68	**1.2.1.6**	**TAB Crates and Services**	**156 w**	**8/23/02**	**10/13/05**
69	1.2.1.6.1	Procure TAB crate for prototype TAB/GAB	18 w	8/23/02	1/9/03
70	1.2.1.6.2	Procure VME CPU for prototype TAB/GAB	9.2 w	10/16/02	12/20/02
71	1.2.1.6.3	Specify TAB crate/power/cooling	2.4 w	4/16/04	5/3/04
72	1.2.1.6.4	Procure TAB crate	18.4 w	6/6/05	10/13/05
73	1.2.1.6.5	Procure TAB VME CPU	9.2 w	6/6/05	8/9/05
74	**1.2.1.7**	**L1 Calorimeter Trigger TAB/GAB Prototyping Complete**	**0 w**	**9/29/03**	**9/29/03**
75	**1.2.1.8**	**L1 Calorimeter Trigger TAB/GAB Production and Testing Complete**	**0 w**	**1/5/06**	**1/5/06**
76	**1.2.1.9**	**Prototype Integration**	**55.6 w**	**12/4/02**	**1/26/04**
77	1.2.1.9.1	Develop in-situ exercizing/debugging software	25 w	12/4/02	6/9/03
78	1.2.1.9.2	Prototype integration test ADF+TAB:Columbia	5 w	9/30/03	11/3/03
79	1.2.1.9.3	Prototype integration test ADF+TAB:Saclay	5 w	9/30/03	11/3/03
80	1.2.1.9.4	Prototype integration test ADF+TAB+GAB+TFW: MSU	10 w	11/4/03	1/26/04
81	1.2.1.9.5	Prototype integration test ADF+TAB+GAB+TFW: Columbia	10 w	11/4/03	1/26/04
82	1.2.1.9.6	Prototype integration test ADF+TAB+GAB+TFW: Saclay	10 w	11/4/03	1/26/04
83	**1.2.1.10**	**Pre-Production Integration**	**20.8 w**	**9/1/04**	**2/8/05**
84	1.2.1.10.1	Pre-production integration test ADF+TAB+TFW: MSU	5 w	9/1/04	10/6/04
85	1.2.1.10.2	Pre-production integration test ADF+TAB+TFW: Columbia	5 w	9/1/04	10/6/04
86	1.2.1.10.3	Pre-production integration test ADF+TAB+TFW: Saclay	5 w	9/1/04	10/6/04
87	1.2.1.10.4	Production readiness review	1.2 w	2/1/05	2/8/05
88	**1.2.1.11**	**L1 Cal Online Software**	**125 w**	**1/6/03**	**7/7/05**
89	1.2.1.11.1	ADF online software	125 w	1/6/03	7/7/05
90	1.2.1.11.2	TAB/GAB online software	125 w	1/6/03	7/7/05
91	**1.2.1.12**	**L1 Calorimeter Trigger Production And Testing Complete**	**0 w**	**1/5/06**	**1/5/06**
92	**1.2.2**	**Level 1 Calorimeter Track Matching**	**106.8 w**	**8/1/02**	**9/23/04**
93	**1.2.2.1**	**D0 Reviews**	**18.6 w**	**1/23/03**	**6/3/03**
94	**1.2.2.1.1**	**D0 Internal Design Review Complete**	**0 w**	**1/23/03**	**1/23/03**
95	1.2.2.1.2	D0 Production Readiness Review	1.2 w	5/27/03	6/3/03
96	**1.2.2.2**	**L1 Trigger Cal-Trk Match R&D MOU (UAZ) Complete**	**0 w**	**11/1/02**	**11/1/02**

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Task | Split | Milestone | Completed Milestone
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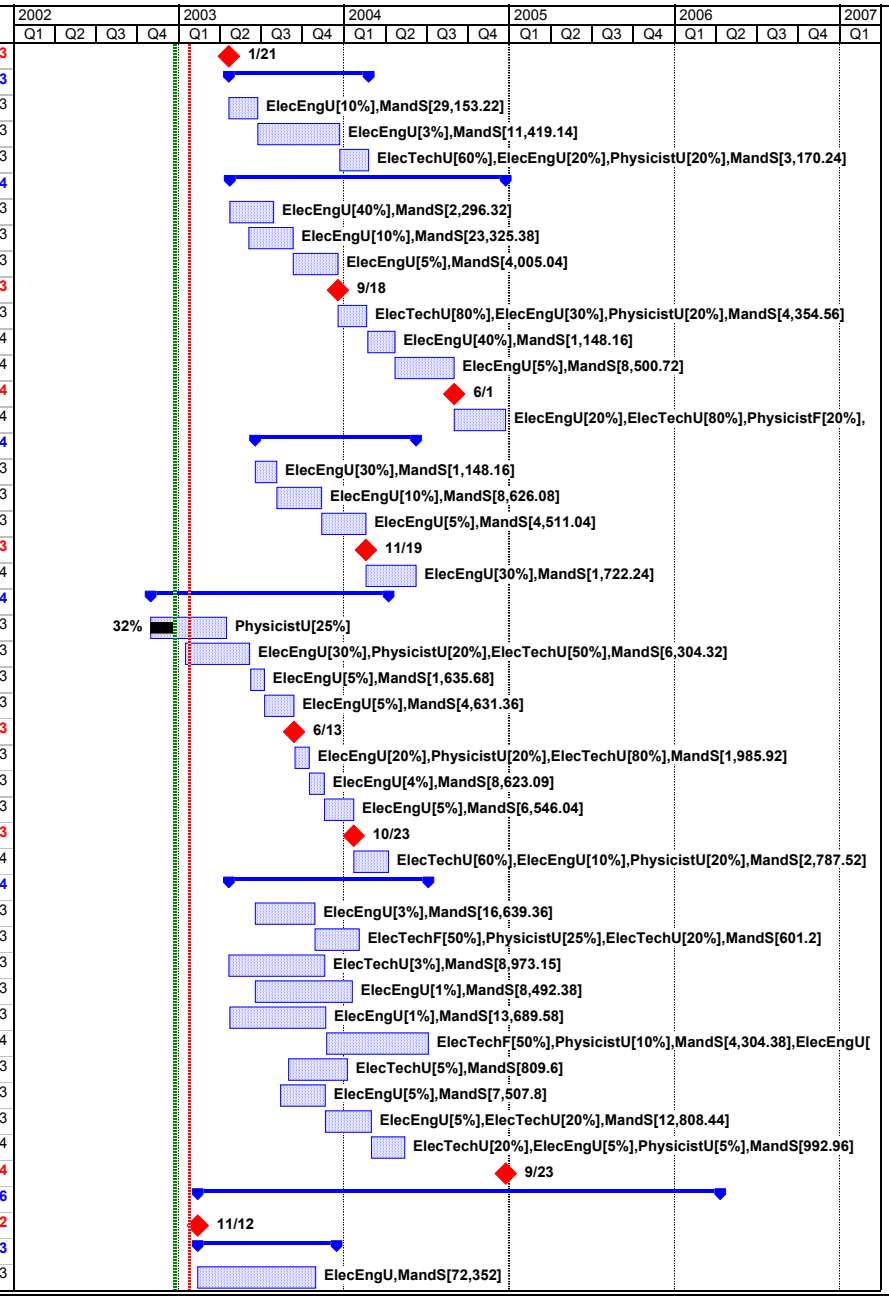
ID	WBS	Task Name	Duration	Start	Finish
97	1.2.2.3	L1 Trigger Cal-Trk Match Equipment MOU (UAZ) Complete	0 w	1/21/03	1/21/03
98	1.2.2.4	SLDB	43.6 w	1/21/03	11/25/03
99	1.2.2.4.1	Procure SLDB parts	9.2 w	1/21/03	3/25/03
100	1.2.2.4.2	Fabricate and assemble SLDBs	25.2 w	3/26/03	9/22/03
101	1.2.2.4.3	Test SLDBs	9.2 w	9/23/03	11/25/03
102	1.2.2.5	MTCxx	84 w	1/23/03	9/23/04
103	1.2.2.5.1	(Small) design changes to MTCxx	13.8 w	1/23/03	4/29/03
104	1.2.2.5.2	Procure MTCxx parts	13.8 w	3/6/03	6/11/03
105	1.2.2.5.3	Fabricate and assemble prototype MTCxx	13.8 w	6/12/03	9/18/03
106	1.2.2.5.4	Fabrication and Assembly of L1 Trigger Cal-Trk Match Prototype MTC	0 w	9/18/03	9/18/03
107	1.2.2.5.5	Test prototype MTCxx	9.2 w	9/19/03	11/21/03
108	1.2.2.5.6	Final design changes to MTCxx	6.8 w	11/24/03	1/22/04
109	1.2.2.5.7	Fabricate and assemble MTCxx's	18.4 w	1/23/04	6/1/04
110	1.2.2.5.8	Fabrication and Assembly of L1 Trigger Cal-Trk Match MTCxx's Comp	0 w	6/1/04	6/1/04
111	1.2.2.5.9	Test MTCxx's	16 w	6/2/04	9/23/04
112	1.2.2.6	MTCM	48.2 w	3/20/03	3/9/04
113	1.2.2.6.1	(Small) design changes to MTCM	6.8 w	3/20/03	5/6/03
114	1.2.2.6.2	Procure MTCM parts	13.8 w	5/7/03	8/13/03
115	1.2.2.6.3	Fabricate and assemble MTCMs	13.8 w	8/14/03	11/19/03
116	1.2.2.6.4	Fabrication and Assembly of L1 Trigger Cal-Trk Match MTCM's Compl	0 w	11/19/03	11/19/03
117	1.2.2.6.5	Test MTCMs	13.8 w	11/20/03	3/9/04
118	1.2.2.7	MT Flavor Board	70.6 w	8/1/02	1/8/04
119	1.2.2.7.1	Design prototype cal-trk algorithm	22 w	8/1/02	1/15/03
120	1.2.2.7.2	Design prototype MTFB	18.4 w	10/17/02	3/7/03
121	1.2.2.7.3	Procure prototype MTFB parts	4.6 w	3/10/03	4/9/03
122	1.2.2.7.4	Fabricate and assemble prototype MTFB	9.2 w	4/10/03	6/13/03
123	1.2.2.7.5	Fabrication and Assembly of L1 Trigger Cal-Trk Match Prototype MTF	0 w	6/13/03	6/13/03
124	1.2.2.7.6	Test prototype MTFB	4.6 w	6/16/03	7/17/03
125	1.2.2.7.7	Procure MTFB parts	4.6 w	7/18/03	8/19/03
126	1.2.2.7.8	Fabricate and assemble MTFB	9.2 w	8/20/03	10/23/03
127	1.2.2.7.9	Fabrication and Assembly of L1 Trigger Cal-Trk Match MTFB's Compl	0 w	10/23/03	10/23/03
128	1.2.2.7.10	Test MTFB	9.2 w	10/24/03	1/8/04
129	1.2.2.8	Infrastructure	60.4 w	1/21/03	4/5/04
130	1.2.2.8.1	Procure cables and connectors	18.4 w	3/20/03	7/29/03
131	1.2.2.8.2	Assemble cables	13.8 w	7/30/03	11/4/03
132	1.2.2.8.3	Procure VME crates	30 w	1/21/03	8/20/03
133	1.2.2.8.4	Procure VME processors	30 w	3/20/03	10/20/03
134	1.2.2.8.5	Procure power supplies	30 w	1/23/03	8/22/03
135	1.2.2.8.6	Assemble and test power supplies	30 w	8/25/03	4/5/04
136	1.2.2.8.7	Procure MSPLIT crates, backplanes, and power supplies	18.4 w	6/2/03	10/9/03
137	1.2.2.8.8	Procure MSPLIT card parts	13.8 w	5/15/03	8/21/03
138	1.2.2.8.9	Fabricate and assemble MSPLIT cards	13.8 w	8/22/03	12/1/03
139	1.2.2.8.10	Test MSPLIT cards	9.2 w	12/2/03	2/13/04
140	1.2.2.9	L1 Trigger Cal-Trk Match Production and Testing Completed	0 w	9/23/04	9/23/04
141	1.2.3	Level 1 Tracking	155.4 w	11/12/02	1/10/06
142	1.2.3.1	Prototype L1 Central Track Trigger Algorithm Coded And Simulated With I	0 w	11/12/02	11/12/02
143	1.2.3.2	Develop Target CTT Algorithm	41.2 w	11/13/02	9/15/03
144	1.2.3.2.1	Develop firmware for target CTT algorithm	35 w	11/13/02	7/31/03



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Task | Split | Milestone | Completed Milestone
Critical Task | Progress | Critical Milestone | Summary

Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
145	1.2.3.2.2	Fit CTT firmware in selected FPGA, do timing studies	6.2 w	8/1/03	9/15/03
146	**1.2.3.3**	**Target L1 Central Track Trigger Algorithm Firmware Coded And Simulated**	**0 w**	**9/15/03**	**9/15/03**
147	**1.2.3.4**	**Develop Test Procedures**	**6.8 w**	**9/16/03**	**10/31/03**
148	1.2.3.4.1	Develop test procedures	6.8 w	9/16/03	10/31/03
149	1.2.3.4.2	Develop test procedures	6.8 w	9/16/03	10/31/03
150	**1.2.3.5**	**DFEA Prototype I**	**36.2 w**	**9/16/03**	**6/9/04**
151	1.2.3.5.1	Layout	10 w	9/16/03	11/24/03
152	1.2.3.5.2	Procure prototype DFEA FPGAs	15 w	9/16/03	1/9/04
153	1.2.3.5.3	Procure prototype DFEA FPGAs	15 w	9/16/03	1/9/04
154	1.2.3.5.4	Procure prototype DFEA parts	5 w	11/25/03	1/9/04
155	1.2.3.5.5	Procure prototype DFEA parts	5 w	11/25/03	1/9/04
156	1.2.3.5.6	PCB fabrication	2.4 w	1/12/04	1/28/04
157	1.2.3.5.7	PCB fabrication	2.4 w	1/12/04	1/28/04
158	1.2.3.5.8	Assembly	3.8 w	1/29/04	2/24/04
159	1.2.3.5.9	Assembly	3.8 w	1/29/04	2/24/04
160	1.2.3.5.10	Test	15 w	2/25/04	6/9/04
161	1.2.3.5.11	Test	15 w	2/25/04	6/9/04
162	**1.2.3.6**	**DFEA Prototype II**	**19.2 w**	**3/16/04**	**7/29/04**
163	1.2.3.6.1	Layout	5 w	3/16/04	4/19/04
164	1.2.3.6.2	PCB fabrication	2.4 w	4/20/04	5/5/04
165	1.2.3.6.3	PCB fabrication	2.4 w	4/20/04	5/5/04
166	1.2.3.6.4	Procure parts for the prototpes	3.8 w	4/6/04	4/30/04
167	1.2.3.6.5	Procure parts for the prototpes	3.8 w	4/6/04	4/30/04
168	1.2.3.6.6	Assembly	2.4 w	5/3/04	5/18/04
169	1.2.3.6.7	Assembly	2.4 w	5/3/04	5/18/04
170	1.2.3.6.8	Test	10 w	5/19/04	7/29/04
171	1.2.3.6.9	Test	10 w	5/19/04	7/29/04
172	1.2.3.6.10	Test at Fermilab	7.6 w	6/3/04	7/27/04
173	1.2.3.6.11	Test at Fermilab	7.6 w	6/3/04	7/27/04
174	1.2.3.6.12	Test at Fermilab	7.6 w	6/3/04	7/27/04
175	1.2.3.6.13	Test at Fermilab	7.6 w	6/3/04	7/27/04
176	1.2.3.6.14	Test at Fermilab	7.6 w	6/3/04	7/27/04
177	**1.2.3.6.15**	**L1 Central Track Trigger DFEA Prototype Tested At Fermilab**	**0 w**	**7/27/04**	**7/27/04**
178	**1.2.3.7**	**DFEA Production**	**59 w**	**10/25/04**	**1/10/06**
179	1.2.3.7.1	Procure FPGAs	27 w	12/14/04	7/1/05
180	1.2.3.7.2	Procure FPGAs	27 w	12/14/04	7/1/05
181	1.2.3.7.3	Procure FPGAs	27 w	11/3/04	5/24/05
182	1.2.3.7.21	Production readiness review	1.4 w	10/25/04	11/2/04
183	1.2.3.7.4	Design, layout	5.4 w	11/3/04	12/13/04
184	1.2.3.7.5	Procure production parts	8 w	12/14/04	2/17/05
185	1.2.3.7.6	Procure production parts	8 w	12/14/04	2/17/05
186	1.2.3.7.7	PCB Fabricate	8 w	1/13/05	3/10/05
187	1.2.3.7.8	PCB Fabricate	8 w	1/13/05	3/10/05
188	1.2.3.7.9	Assembly	5.4 w	3/11/05	4/18/05
189	1.2.3.7.10	Assembly	5.4 w	3/11/05	4/18/05
190	1.2.3.7.11	Bench test	10.8 w	4/19/05	7/5/05
191	1.2.3.7.12	Bench test	10.8 w	4/19/05	7/5/05
192	1.2.3.7.13	Test at Fermilab	33.8 w	5/3/05	1/10/06

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ID	WBS	Task Name	Duration	Start	Finish
193	1.2.3.7.14	Test at Fermilab	33.8 w	5/3/05	1/10/06
194	1.2.3.7.15	Test at Fermilab	33.8 w	5/3/05	1/10/06
195	1.2.3.7.16	Test at Fermilab	33.8 w	5/3/05	1/10/06
196	1.2.3.7.17	Test at Fermilab	33.8 w	5/3/05	1/10/06
197	1.2.3.7.18	Board rework	33.8 w	5/3/05	1/10/06
198	1.2.3.7.19	L1 Central Track Trigger Production And Testing Complete	0 w	1/10/06	1/10/06
199	1.2.9	L1 Trigger Upgrade Production and Testing Complete	0 w	1/10/06	1/10/06
200	1.2.4	Level 2 Beta Processor	69.2 w	8/9/04	1/5/06
201	1.2.4.1	Finalize Targets For Run2b Beta Upgrades	44.8 w	8/9/04	7/5/05
202	1.2.4.1.1	Assess Performance of Run2a Betas	9.2 w	8/9/04	10/12/04
203	1.2.4.1.2	Assess Performance of Run2a Betas	9.2 w	8/9/04	10/12/04
204	1.2.4.1.3	Develop & Simulate New Run2b Algortihms	34.4 w	10/13/04	6/24/05
205	1.2.4.1.4	Develop & Simulate New Run2b Algortihms	34.4 w	10/13/04	6/24/05
206	1.2.4.1.5	Develop & Simulate New Run2b Algortihms	34.4 w	10/13/04	6/24/05
207	1.2.4.1.6	Production readiness review	1.2 w	6/27/05	7/5/05
208	1.2.4.2	Develop Prototype	23.2 w	11/22/04	5/16/05
209	1.2.4.2.1	Survey processor market, chose candidate SBCs	4.6 w	11/22/04	1/4/05
210	1.2.4.2.2	Evaluate software impact of new SBCs	4.6 w	11/22/04	1/4/05
211	1.2.4.2.3	Evaluate firmware impact of new SBCs (function)	4.6 w	11/22/04	1/4/05
212	1.2.4.2.4	Evaluate firmware impact of new SBCs (coding)	4.6 w	11/22/04	1/4/05
213	1.2.4.2.5	Acquire 2 new processor SBCs for evaluation	13.8 w	1/5/05	4/12/05
214	1.2.4.2.6	Design Disk adapter	4.6 w	1/5/05	2/7/05
215	1.2.4.2.7	Specify/Acquire adapter components	4.6 w	2/8/05	3/10/05
216	1.2.4.2.8	Layout adapter, generate artwork, etc	2.4 w	3/11/05	3/28/05
217	1.2.4.2.9	Find manufacturer for disk adapters	2.4 w	3/29/05	4/13/05
218	1.2.4.2.10	Produce prototype adapters	3.4 w	4/14/05	5/6/05
219	1.2.4.2.11	Test prototype adapters	1.2 w	5/9/05	5/16/05
220	1.2.4.2.12	Level 2 Beta Trigger Prototype Preparation Complete	0 w	5/16/05	5/16/05
221	1.2.4.3	Test Prototype	13.8 w	5/17/05	8/23/05
222	1.2.4.3.1	Verify software compatibiity / hardware interface patches	9.2 w	5/17/05	7/21/05
223	1.2.4.3.2	Firmware support and tuning	9.2 w	5/17/05	7/21/05
224	1.2.4.3.3	Verify processor operability in Beta motherboards	9.2 w	5/17/05	7/21/05
225	1.2.4.3.4	Shadow mode studies	4.6 w	7/22/05	8/23/05
226	1.2.4.3.5	Level 2 Beta Trigger Prototype Testing Complete	0 w	8/23/05	8/23/05
227	1.2.4.4	Assemble Production Processors	22 w	7/22/05	1/5/06
228	1.2.4.4.1	Produce Disk Adapters	3.4 w	7/22/05	8/15/05
229	1.2.4.4.2	Finalize Run2b SBC order	2.4 w	8/24/05	9/9/05
230	1.2.4.4.3	Acquire Run2b SBCs	13.8 w	9/12/05	12/19/05
231	1.2.4.4.5	Test production disk adapters	1.2 w	8/16/05	8/23/05
232	1.2.4.4.4	Test SBCs	1.2 w	12/20/05	1/5/06
233	1.2.4.4.6	L2 Beta Trigger Production And Testing Complete	0 w	1/5/06	1/5/06
234	1.2.5	Silicon Track Trigger Upgrade	137.2 w	1/21/03	10/17/05
235	1.2.5.1	Place Parts Orders for L2 Silicon Track Trigger	0 w	4/1/03	4/1/03
236	1.2.5.20	Production Readiness Review	1.2 w	5/2/05	5/9/05
237	1.2.5.2	Begin L2 Silicon Track Trigger Run IIb PCB Production	0 w	5/9/05	5/9/05
238	1.2.5.3	L2 Silicon Track Trigger Run IIb Inputs Finalized	0 w	9/2/03	9/2/03
239	1.2.5.4	VME Motherboard	123.2 w	4/1/03	9/19/05
240	1.2.5.4.1	Place orders	24 h	4/1/03	4/3/03

Project: Run IIb trigger Schedule
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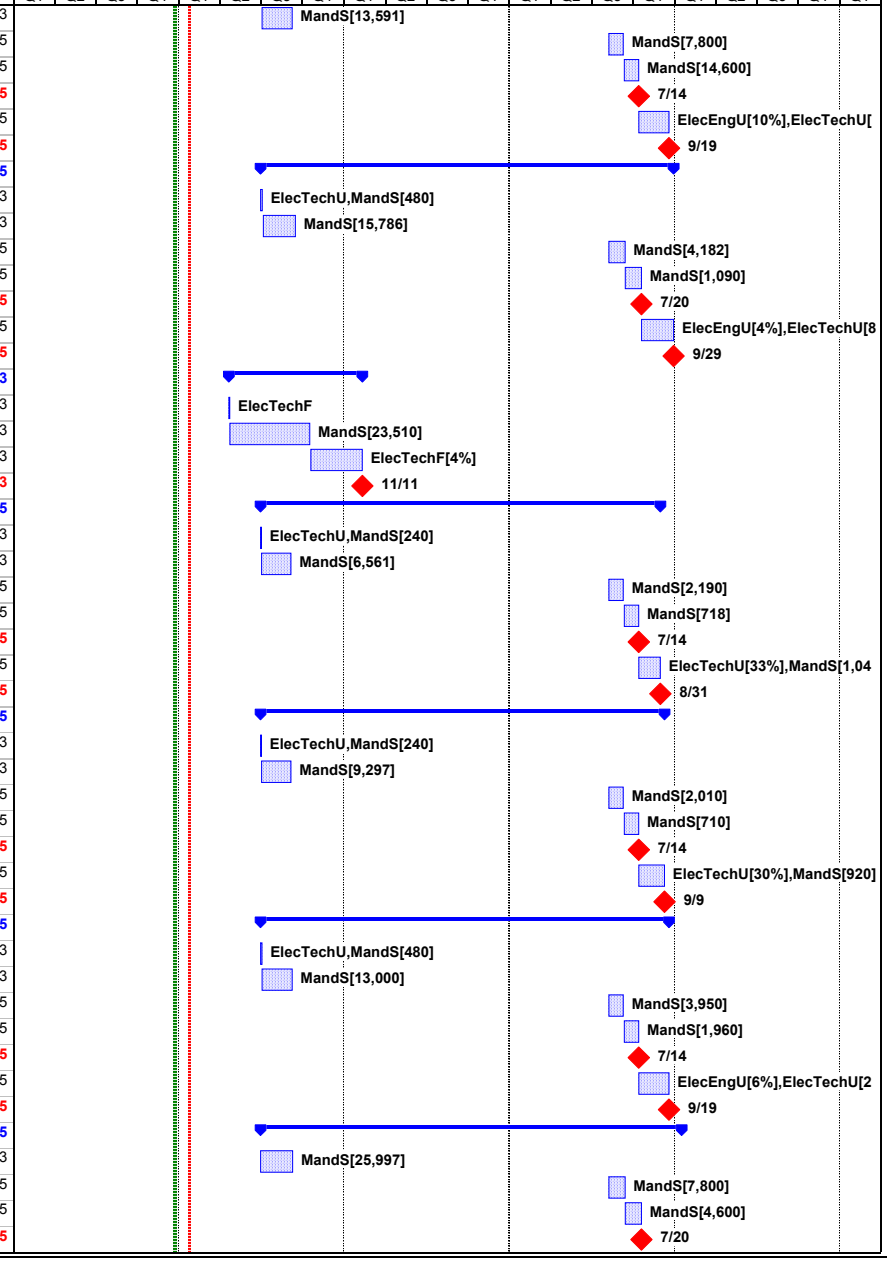
Task | Split | Milestone | Completed Milestone
Critical Task | Progress | Critical Milestone | Summary

Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
241	1.2.5.4.2	Parts	9.2 w	4/4/03	6/9/03
242	1.2.5.4.3	Fabricate PCB	4.6 w	5/10/05	6/10/05
243	1.2.5.4.4	Assemble	4.6 w	6/13/05	7/14/05
244	1.2.5.4.5	All L2 Silicon Track Trigger Motherboards Delivered	0 w	7/14/05	7/14/05
245	1.2.5.4.6	Test	9.2 w	7/15/05	9/19/05
246	1.2.5.4.8	L2 Silicon Track Trigger Motherboard Production Complete	0 w	9/19/05	9/19/05
247	1.2.5.5	STC Module	124.8 w	4/1/03	9/29/05
248	1.2.5.5.1	Place orders	32 h	4/1/03	4/4/03
249	1.2.5.5.2	Parts	10 w	4/7/03	6/16/03
250	1.2.5.5.3	Fabricate PCB	5 w	5/10/05	6/14/05
251	1.2.5.5.4	Assemble	5 w	6/15/05	7/20/05
252	1.2.5.5.5	All L2 Silicon Track Trigger STCs Delivered	0 w	7/20/05	7/20/05
253	1.2.5.5.6	Test	10 w	7/21/05	9/29/05
254	1.2.5.5.8	L2 Silicon Track Trigger STC Production Complete	0 w	9/29/05	9/29/05
255	1.2.5.6	VTM	41.6 w	1/21/03	11/11/03
256	1.2.5.6.1	Place order	16 h	1/21/03	1/22/03
257	1.2.5.6.2	Production	25 w	1/23/03	7/18/03
258	1.2.5.6.3	Test	16.2 w	7/21/03	11/11/03
259	1.2.5.6.5	VTM Production Complete for L2 Silicon Track Trigger	0 w	11/11/03	11/11/03
260	1.2.5.7	Link Transmitter Board	120.8 w	4/1/03	8/31/05
261	1.2.5.7.1	Place orders	16 h	4/1/03	4/2/03
262	1.2.5.7.2	Parts	9.2 w	4/3/03	6/6/03
263	1.2.5.7.3	Fabricate PCB	4.6 w	5/10/05	6/10/05
264	1.2.5.7.4	Assemble	4.6 w	6/13/05	7/14/05
265	1.2.5.7.5	All L2 Silicon Track Trigger LTBs Delivered	0 w	7/14/05	7/14/05
266	1.2.5.7.6	Test	6.8 w	7/15/05	8/31/05
267	1.2.5.7.8	L2 Silicon Track Trigger LTB Production Complete	0 w	8/31/05	8/31/05
268	1.2.5.8	Link Receiver Board	122 w	4/1/03	9/9/05
269	1.2.5.8.1	Place orders	16 h	4/1/03	4/2/03
270	1.2.5.8.2	Parts	9.2 w	4/3/03	6/6/03
271	1.2.5.8.3	Fabricate PCB	4.6 w	5/10/05	6/10/05
272	1.2.5.8.4	Assemble	4.6 w	6/13/05	7/14/05
273	1.2.5.8.5	All L2 Silicon Track Trigger LRBs Delivered	0 w	7/14/05	7/14/05
274	1.2.5.8.6	Test	8 w	7/15/05	9/9/05
275	1.2.5.8.8	L2 Silicon Track Trigger LRB Production Complete	0 w	9/9/05	9/9/05
276	1.2.5.9	BC Module	123.2 w	4/1/03	9/19/05
277	1.2.5.9.1	Place orders	24 h	4/1/03	4/3/03
278	1.2.5.9.2	Parts	9.2 w	4/4/03	6/9/03
279	1.2.5.9.3	Fabricate PCB	4.6 w	5/10/05	6/10/05
280	1.2.5.9.4	Assemble	4.6 w	6/13/05	7/14/05
281	1.2.5.9.5	All L2 Silicon Track Trigger BCs Delivered	0 w	7/14/05	7/14/05
282	1.2.5.9.6	Test	9.2 w	7/15/05	9/19/05
283	1.2.5.9.8	L2 Silicon Track Trigger BC Production Complete	0 w	9/19/05	9/19/05
284	1.2.5.10	TFC Module	127.2 w	4/1/03	10/17/05
285	1.2.5.10.1	Parts	10 w	4/1/03	6/10/03
286	1.2.5.10.2	Fabricate PCB	5 w	5/10/05	6/14/05
287	1.2.5.10.3	Assemble and rework	5 w	6/15/05	7/20/05
288	1.2.5.10.4	All L2 Silicon Track Trigger TFCs Delivered	0 w	7/20/05	7/20/05



Project: Run IIb trigger Schedule
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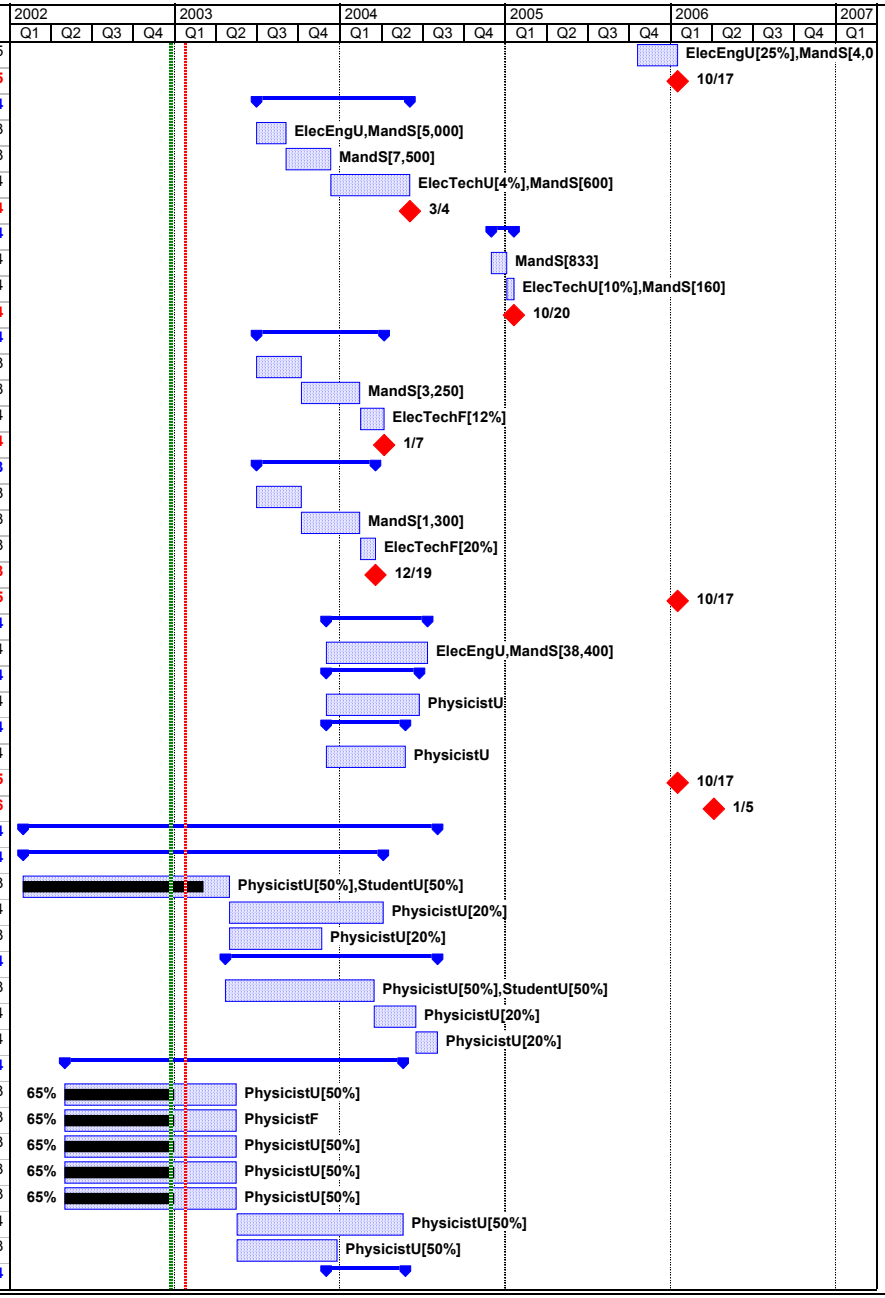
Task | Split | Milestone | Completed Milestone
Critical Task | Progress | Critical Milestone | Summary

Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
289	1.2.5.10.5	Test	12.4 w	7/21/05	10/17/05
290	1.2.5.10.7	L2 Silicon Track Trigger TFC Production Complete	0 w	10/17/05	10/17/05
291	1.2.5.11	Hotlink Repeaters	46 w	4/1/03	3/4/04
292	1.2.5.11.1	Design & prototyping	9.2 w	4/1/03	6/4/03
293	1.2.5.11.2	Production	13.8 w	6/5/03	9/11/03
294	1.2.5.11.3	Test	23 w	9/12/03	3/4/04
295	1.2.5.11.5	L2 Silicon Track Trigger Hotlink Repeaters Complete	0 w	3/4/04	3/4/04
296	1.2.5.12	LVDS Cables	7 w	9/1/04	10/20/04
297	1.2.5.12.1	Fabricate	4.6 w	9/1/04	10/4/04
298	1.2.5.12.2	Test	2.4 w	10/5/04	10/20/04
299	1.2.5.12.4	L2 Silicon Track Trigger LVDS Cable Production Complete	0 w	10/20/04	10/20/04
300	1.2.5.13	Splitters	38 w	4/1/03	1/7/04
301	1.2.5.13.1	Go out for bids	13.8 w	4/1/03	7/8/03
302	1.2.5.13.2	Procure	18.4 w	7/9/03	11/14/03
303	1.2.5.13.3	Test	5.8 w	11/17/03	1/7/04
304	1.2.5.13.5	L2 Silicon Track Trigger Splitter Procurement Complete	0 w	1/7/04	1/7/04
305	1.2.5.14	Fibers	36.8 w	4/1/03	12/19/03
306	1.2.5.14.1	Go out for bids	13.8 w	4/1/03	7/8/03
307	1.2.5.14.2	Procure	18.4 w	7/9/03	11/14/03
308	1.2.5.14.3	Test	4.6 w	11/17/03	12/19/03
309	1.2.5.14.5	L2 Silicon Track Trigger Fiber Procurement Complete	0 w	12/19/03	12/19/03
310	1.2.5.15	L2 Silicon Track Trigger Hardware Production Complete	0 w	10/17/05	10/17/05
311	1.2.5.16	STC firmware	30 w	9/2/03	4/12/04
312	1.2.5.16.1	Update STC firmware	30 w	9/2/03	4/12/04
313	1.2.5.17	TFC Code	27.6 w	9/2/03	3/25/04
314	1.2.5.17.1	Change TFC code	27.6 w	9/2/03	3/25/04
315	1.2.5.18	Downloading & Monitoring	23 w	9/2/03	2/23/04
316	1.2.5.18.1	Update downloading & monitoring software	23 w	9/2/03	2/23/04
317	1.2.5.19	L2 Silicon Track Trigger Production and Testing Complete	0 w	10/17/05	10/17/05
318	1.2.8	L2 Trigger Upgrade Production and Testing Complete	0 w	1/5/06	1/5/06
319	1.2.6	Trigger Simulation	123.8 w	11/1/01	5/4/04
320	1.2.6.1	L1 Cal Simulation Software	106.8 w	11/1/01	1/5/04
321	1.2.6.1.1	Develop prototype L1Cal trigger algorithm	60.8 w	11/1/01	1/30/03
322	1.2.6.1.2	Optimize algorithm performance with simulations	46 w	1/31/03	1/5/04
323	1.2.6.1.3	Integrate algorithm into Dzero Trigger Simulation	28.8 w	1/31/03	8/22/03
324	1.2.6.2	L1 Cal/Track Match Simulation Software	64.4 w	1/22/03	5/4/04
325	1.2.6.2.1	Develop prototype L1Cal/Track match trigger algorithm	46 w	1/22/03	12/16/03
326	1.2.6.2.2	Optimize algorithm performance with simulations	11.6 w	12/17/03	3/17/04
327	1.2.6.2.3	Integrate algorithm into Dzero Trigger Simulation	6.8 w	3/18/04	5/4/04
328	1.2.6.3	L1CTT Simulation	101.6 w	2/1/02	2/18/04
329	1.2.6.3.1	Develop prototype CTT algorithm	51.6 w	2/1/02	2/14/03
330	1.2.6.3.2	Develop prototype CTT algorithm	51.6 w	2/1/02	2/14/03
331	1.2.6.3.3	Develop prototype CTT algorithm	51.6 w	2/1/02	2/14/03
332	1.2.6.3.4	Develop prototype CTT algorithm	51.6 w	2/1/02	2/14/03
333	1.2.6.3.5	Develop prototype CTT algorithm	51.6 w	2/1/02	2/14/03
334	1.2.6.3.6	Optimize algorithm performance with simulations	50 w	2/17/03	2/18/04
335	1.2.6.3.7	Integrate algorithm into Dzero Trigger Simulation	31.2 w	2/17/03	9/25/03
336	1.2.6.4	STT Simulator	23 w	9/2/03	2/23/04



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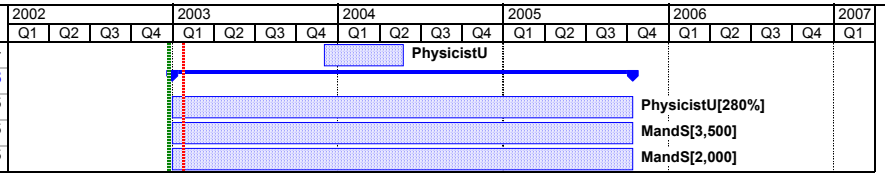
Task | Split | Milestone | Completed Milestone
Critical Task | Progress | Critical Milestone | Summary

Run IIb Trigger

ID	WBS	Task Name	Duration	Start	Finish
337	1.2.6.4.1	Update simulator	23 w	9/2/03	2/23/04
338	**1.2.7**	**Administration**	**138 w**	**10/1/02**	**7/14/05**
339	1.2.7.1	Project Management	138 w	10/1/02	7/14/05
340	1.2.7.2	Travel vendor and collaborator visits	138 w	10/1/02	7/14/05
341	1.2.7.3	Shipping costs	138 w	10/1/02	7/14/05



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Critical Task		Progress		Critical Milestone		Summary	