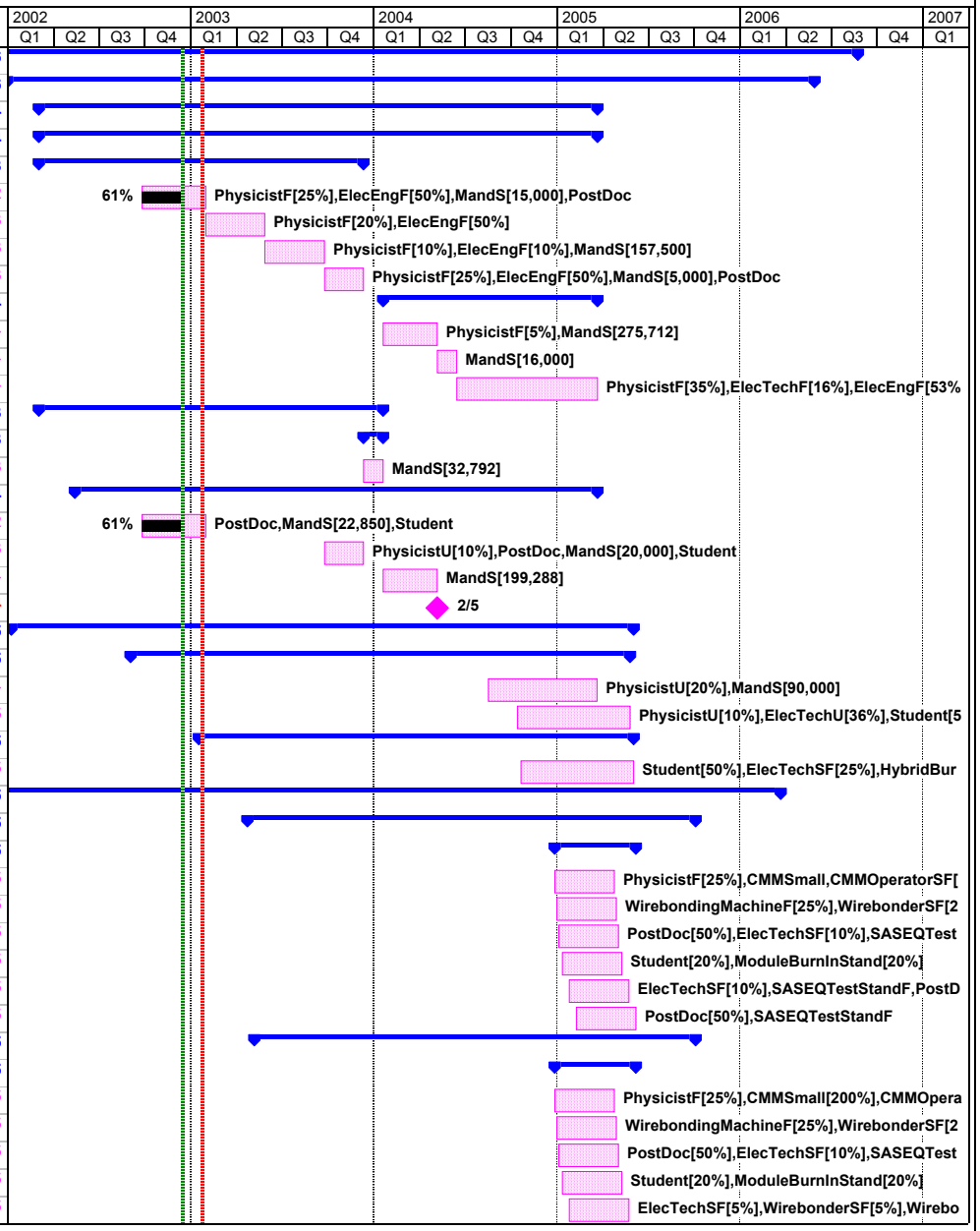
Run IIb Silicon Tracker
Critical Path

ID	Task Name	Duration	Start	Finish
1	**Run IIb Silicon**	**280.7 w**	**10/2/00**	**5/25/06**
106	**Readout System**	**218 w**	**10/1/01**	**2/27/06**
107	**SVX4 Chips**	**151.8 w**	**12/3/01**	**12/21/04**
108	**SVX4 Chips (at Fermilab)**	**151.8 w**	**12/3/01**	**12/21/04**
109	**Fermilab R&D Tasks**	**88.2 w**	**12/3/01**	**9/11/03**
112	Test SVX4 prototype chips by D0 group	17.8 w	6/27/02	10/31/02
113	Develop second prototype chip	14.8 w	11/1/02	2/26/03
114	Produce second prototype chip	84 d	2/27/03	6/25/03
115	Test second prototype by D0 group	10.8 w	6/26/03	9/11/03
117	**Fermilab Production Tasks**	**58.2 w**	**10/21/03**	**12/21/04**
118	Produce final wafers(Fermilab)	13.6 w	10/21/03	2/5/04
119	Wafer backgrinding/plating	5.4 w	2/6/04	3/15/04
120	Test chips on wafers	39.2 w	3/16/04	12/21/04
122	**SVX4 Chip Development and Testing at LBL**	**93.6 w**	**12/3/01**	**10/20/03**
128	**SVX4 Chip Production Tasks**	**5.4 w**	**9/12/03**	**10/20/03**
129	Production chip layout and design (LBL)	5.4 w	9/12/03	10/20/03
130	**SVX4 Chip Testing and Production MRI Grant**	**142.8 w**	**2/13/02**	**12/21/04**
132	Test SVX4 prototype chips MRI	17.8 w	6/27/02	10/31/02
133	Test second prototype chips MRI	10.8 w	6/26/03	9/11/03
134	Produce final wafers MRI	13.6 w	10/21/03	2/5/04
137	**SVX4 Wafers Delivered**	**0 w**	**2/5/04**	**2/5/04**
197	**L2-L5 Hybrids**	**168 w**	**10/9/01**	**3/3/05**
208	**MRI Production and Testing of Hybrids**	**135 w**	**6/4/02**	**2/24/05**
216	Stuff and wire-bond hybrids	30 w	5/18/04	12/20/04
217	Perform initial functionality test (MRI)	30 w	7/15/04	2/24/05
219	**Production (Fermilab)**	**116.8 w**	**10/18/02**	**3/3/05**
224	Perform hybrid burn-in test	30 w	7/22/04	3/3/05
589	**Detector Production and Testing**	**260.3 w**	**10/2/00**	**12/22/05**
692	**10/10 Axial Modules**	**122.1 w**	**1/23/03**	**7/5/05**
694	**South**	**21.2 w**	**9/27/04**	**3/8/05**
696	Align and glue sensors to hybrid	15 w	9/27/04	1/24/05
697	Wirebond sensors to hybrid	15 w	10/1/04	1/28/05
699	Debug sensor module	15 w	10/5/04	2/1/05
700	Burn-in sensor modules	15 w	10/12/04	2/8/05
701	Evaluate and repair sensor modules	15 w	10/26/04	2/22/05
702	Perform quality assurance tests	15 w	11/9/04	3/8/05
716	**20/20 Axial Modules**	**120.1 w**	**2/6/03**	**7/5/05**
718	**South**	**21.2 w**	**9/27/04**	**3/8/05**
720	Align and glue sensors to hybrid	15 w	9/27/04	1/24/05
721	Wirebond sensors to hybrid	15 w	10/1/04	1/28/05
723	Debug sensor modules	15 w	10/5/04	2/1/05
724	Burn-in sensor modules	15 w	10/12/04	2/8/05
725	Evaluate and repair sensor modules	15 w	10/26/04	2/22/05



Project: Run IIb Silicon Schedule
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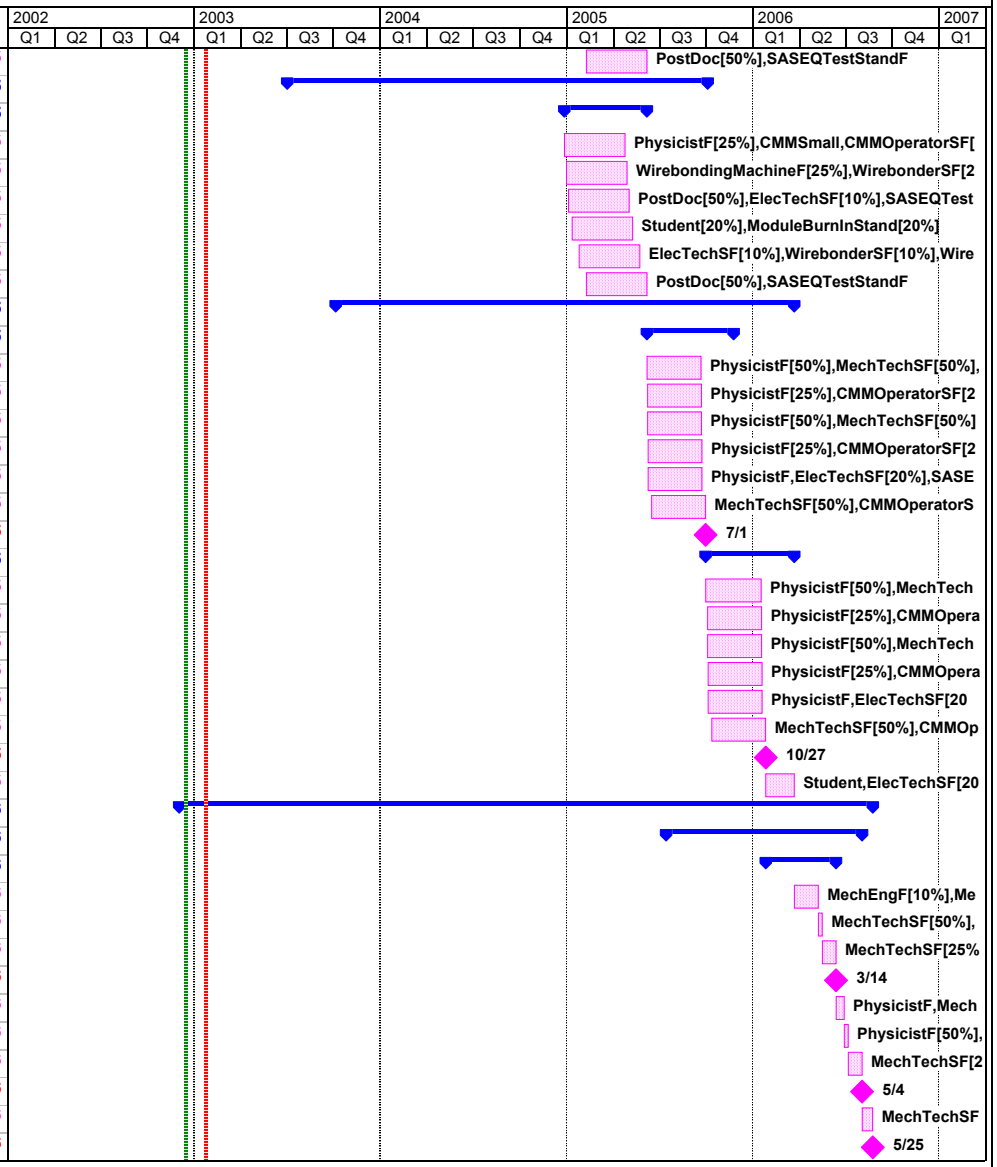
Date: 10/23/02

Task | Critical Task | Split | Progress | Milestone | Critical Milestone | Completed Milestone | Summary

Run IIb Silicon Tracker
Critical Path

ID	Task Name	Duration	Start	Finish
726	Perform quality assurance tests	15 w	11/9/04	3/8/05
738	**10/10 Stereo Modules**	**112.1 w**	**4/3/03**	**7/5/05**
740	**South**	**21.2 w**	**9/27/04**	**3/8/05**
742	Align and glue sensors to hybrid	15 w	9/27/04	1/24/05
743	Wirebond sensors to hybrid	15 w	10/1/04	1/28/05
745	Debug sensor modules	15 w	10/5/04	2/1/05
746	Burn-in sensor modules	15 w	10/12/04	2/8/05
747	Evaluate and repair sensor modules	15 w	10/26/04	2/22/05
748	Perform quality assurance tests	15 w	11/9/04	3/8/05
793	**Assemble Modules Onto Staves**	**122.9 w**	**7/7/03**	**12/22/05**
803	**South**	**24 w**	**3/8/05**	**8/25/05**
805	Install axial sensor modules on cores	15 w	3/8/05	6/22/05
806	Verify alignment of axial sensor modules	15 w	3/9/05	6/23/05
807	Install stereo sensor modules on cores	15 w	3/9/05	6/23/05
808	Verify alignment of stereo sensor modules	15 w	3/10/05	6/24/05
809	Electrical test of modules on stave core	15 w	3/10/05	6/24/05
810	Attach C-channels to L2-L5 staves	15 w	3/17/05	7/1/05
811	**L2-L5 Silicon Stave Production For South Complete**	**0 w**	**7/1/05**	**7/1/05**
815	**North**	**24 w**	**7/1/05**	**12/22/05**
817	Install axial sensor modules on cores	15 w	7/1/05	10/18/05
818	Verify alignment of axial sensor modules	15 w	7/5/05	10/19/05
819	Install stereo sensor modules on cores	15 w	7/5/05	10/19/05
820	Verify alignment of stereo sensor modules	15 w	7/6/05	10/20/05
821	Electrical test of modules on stave core	15 w	7/6/05	10/20/05
822	Attach C-channels to L2-L5 staves	15 w	7/13/05	10/27/05
823	**L2-L5 Silicon Stave Production For North Complete**	**0 w**	**10/27/05**	**10/27/05**
824	Test L2-L5 staves	7.6 w	10/27/05	12/22/05
828	**Silicon Barrel Assembly**	**184.7 w**	**9/3/02**	**5/25/06**
902	**North Silicon Barrel Assembly**	**52.3 w**	**4/15/05**	**5/4/06**
917	**L2-L5 Barrel Assembly**	**17.6 w**	**10/27/05**	**3/14/06**
919	Install staves in north support cylinder	5 w	12/22/05	2/7/06
920	Verify stave alignment	1.2 w	2/7/06	2/15/06
921	Final electrical test of north layers 2-5	3.8 w	2/15/06	3/14/06
922	**Layer 2-5 North Silicon Complete**	**0 w**	**3/14/06**	**3/14/06**
923	Mate L0-L1 with L2-L5, north	2.4 w	3/14/06	3/30/06
924	Install Radiation monitors	1.2 w	3/30/06	4/7/06
925	Final electrical test of north silicon	3.8 w	4/7/06	5/4/06
926	**North Silicon Complete**	**0 w**	**5/4/06**	**5/4/06**
928	Prepare for shipment	3 w	5/4/06	5/25/06
929	**Silicon Ready To Move To DAB**	**0 w**	**5/25/06**	**5/25/06**



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Task		Split		Milestone		Completed Milestone	
Critical Task		Progress		Critical Milestone		Summary	