L2STT Simulator — 2 March 2001

STR

** IOGen - STT code has been modified to comply to the IOGen requirements and has been installed in the tsim_l1l2 package , as a component of l1l2 .

** Status at present : running .

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General debugging issues

** STT ran in tsim_1112 with 30 “higgs” events
(then crash in CTT)

Further debugging on the way– probably evt 31 was too big, stt may overflow max memory available , etc . -

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Specific STT debubbing issues:

** STT Tracks in high multiplicity events : higher Chi2??

** Still issues with Smt geometry - version changes need to be managed differently (another source of bad Chi2) .

** need a new ‘single muon ‘ file for tests with STT.

Old file not valid anymore.

No many MC files available with the needed info so far for running in tsim_l1l2, since stt has never been in yet.

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** STT outputs IOGen Object:

"STTTrack" : b, kappa, Phi0, Chi2, Pt, Rc

** STT inputs:

1 - SmtRawData Chunk (UnpDataChunk) –

- was SmtData –

2 – L1FT : needs to be IOGen Object

L1_FT UnpdataChunk at present

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** Next:

- insert L1FT IOGen object
- Provide STT output with the 32 bit words format
- Implement an STT_analyze (to produce a Root file)
- Setup a mode to run STT as ‘standalone’ in the new framework of tsim_l1l2 [may be useful for STT developers
- Clean up and test before releasing stt for general trigger simulator users

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simulator (standalone) status :

Since t01.32.00 CTT tracks had disappeared.

Mystery solved. Temporary fix committed to CVS.

STT will disappear as a standalone processor soon. The extended frame where it will belong (tsim_l1l2) is more disturbed and exposed to bugs and interferences, not ideal for development tasks. Hopefully will find an appropriate mode to run stt in tsim_l1l2 with minimal interferences to the outside world