

Updated 27-May-06								
Week #		Sunday	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
0	19-Feb					Rad Survey Open muon shields and EFS Remove BLMs Fix EC beampipe location relative to EC Close ECs, EFS Install EFS platform Remove veto counters		
1	26-Feb		Remove SNEGS Inspect interior of EC beampipes Install collapsible beampipe sections	Open EFS, CFs, ECs Warm Silicon Begin installation of Gap Access Hardware	Complete Installation of Gap Access Hardware Rad Survey Uncable Luminosity Monitor Remove L1Muon power supplies Install DFEA2 power supply cables	Remove Tedlar Membranes Document location of EC beampipe flange	Disconnect Be beampipe Cut EC beampipe ends Dismount Luminosity Monitors	
2	5-Mar		TFW down MCH network upgrades Begin retirement of Run 1 L1Cal Uncable N outer H disk Uncable S outer H disk	PCC repaired Disconnect signals and services for L1Cal Remove N outer H disk	Feeder 46B down Helium compressors offline Disconnect signals and services for L1Cal Wipe Be beampipe Uncable N inner H disk Reroute SMT coolant flow Install LV gaps for adapter cards Upgrade of muon control system processors complete	Begin installing infrastructure for L1Cal Uncable front of DFEA2 crates Remove S outer H disk Uncable S inner H disk Install Adapter Cards in N gap	Continue installing infrastructure for L1Cal Remove a DFEA2 crate Remove N inner H disk Install Adapter Cards in N gap Practice run for Layer 0 transportation	
3	12-Mar		Remove second DFEA2 crate Install DFEA2 crates Reinforce mount on S inner H disk Install Adapter Cards in N gap	Connect lower DFEA2 crate LVDS input cables Perform powered test of N readout chain Remove S inner H disk Begin adapter card installation in S gap	Complete installation of L1Cal trigger neck infrastructure Complete checkout of adapter cards in N gap Complete installation of adapter cards in S gap	More PCC challenges Complete LVDS cabling of lower DFEA2 crate Setup for Run 1Ia beampipe extraction	Connect upper DFEA2 crate outputs Retract Be beampipe into ECN	
4	19-Mar		Re-estimate full FIRUS for MCH1 Begin moving L1Cal crates from sidevents to MCH1 Remove N gap hardware Close ECN, EFN Install EFN platform	Complete LVDS cabling of upper DFEA2 crate Remove Run 1Ia Be pipe from Collision Hall S gap electrical contingency	DFEA2 boards installed Complete packaging of Layer 0 at S1Det Dry run with dummy Layer 0 S gap electrical contingency	Transport Layer 0 to DAB Load Layer 0 and Run 1Ib beampipe into ECN Take down platform and scaffolding between NEC and N quad	Open EFN and ECN Install N gap hardware	
5	26-Mar		Prep surfaces of SMT support structure Install N table	Verify Layer 0 mount location HV testing	Install short rails and Brunson on S table Mount stages and align S rails	Begin verifying DFEA2 LVDS connections Align S Layer 0 CF mount ring	ORC inspection for L1CAL Install S Layer 0 CF mount ring on SMT	0700-1600 D0 power outage Detector remains powered down
6	2-Apr	Detector remains powered down	0700-0730 Site-wide power outage Verify S mount location Install short rails and Brunson on N table Mount stages and align N rails	Install N Layer 0 CF mount ring on SMT	Verify N mount location Move Brunson to S gap Align split rails in N gap Prep stages in N gap Remove split N rails Move Brunson to N gap Install and align long rails in S gap Guide long tool through SMT	AFELI walking ones test Testing DFEA2 outputs to CTCC Verify long tool position Remove Brunson from N gap Re-install split rail in N gap Insert Layer 0 into SMT	Crate 0x13 in ZB run Install junction card mounts	
7	9-Apr		Install Run 1Ib Be beampipe Measure capacitance to beampipe	L1Cal ORC approval Install 3 N junction cards	Leak test Layer 0 cooling manifold Install 4 N junction cards Begin noise studies	Install 2 N junction cards and 6 S junction cards	Test inner layer junction card connections Continue noise studies	
8	16-Apr	BLS cable installation complete	CTT timing scan Verify STSX outputs CTT ORC approval Dress inner twisted pair cables Verify N inner layer junction card connections	AFELI boards released for production CTT LVDS outputs verified Install 3N and 4S outer junction cards Verify junction card connections	First Gore LVDS cables arrive Complete junction card installation Verify outer layer junction card connections	Weld EC beampipe stubs	Verify Layer 0 readout	Clued0 and CAB may not be available
9	23-Apr	Clued0 and CAB may not be available	Begin connecting DFEA2 to L1CalTrk and L1Muon Setup to connect spool pieces to Be beampipe	L1CTT installation complete Gore LVDS delivery complete Connect spool pieces and leak check Be to spool joints	Complete leak testing of inner joint Investigate intermittent readout errors Test S H disk HV distribution Install luminosity monitors	Complete Layer 0 readout testing Install S inner H disk	L1Muon central triggers available Install N inner H disk Reconnect H disk cooling and dry gas purge	
10	30-Apr	0700-0730 Site-wide power outage North side A Layer PDT grounding Cable N inner H disk	Online test of L2 splitting/ORing Install L1Cal pleated foil cable PMT swaps in ICD west Cool silicon	First production AFELI bare board delivered North side A Layer PDT grounding Cable S inner H disks	Complete installation of L1Cal LVDS cables Verify H disk connections	SAM V7 Tests Complete H disk readout verification Install beampipe supports Make EC Be beampipe joints	Install Tedlar membrane	
11	7-May			Complete installation of L1Cal pleated foil cables Silicon cooldown complete PMT swaps in ICD west SMT checkout	L1Cal Upgrade Ready for Technical Commissioning SMT checkout	SMT checkout	Muon PDT mod complete SMT checkout	
12	14-May		Remove gap hardware Close ECs, EFS	Repair two PDT wires Install SNEGS and begin pumpdown SMT cosmetics	Extract wire from PDT Leak check SMT noise studies SMT cosmetics?	Extract wire from PDT Install heat tape on SNEGS Activate SNEGS Open EFS Install BLMs	South side A Layer PDT grounding Activate SNEGS ICD maintenance	
13	21-May	Begin detector verification	Tevatron cooldown begins Survey EC ICD maintenance CAL preamp PS maintenance Detector checkout Test forward muon system Resume captain coverage on evening shifts	ICD maintenance Detector checkout	TFW unavailable for scaler studies from 0500 to 0700 Close CFs and EFS Monitor calorimeter for noise Close pit access door Detector checkout	100 AFELI bare boards delivered Extract wire from PDT Install heat tape on SNEGS Leak check SMT noise studies SMT cosmetics?	L1CTT Technical Commissioning Complete Disable wires in PDT 232 Remove SNEG heaters Install S veto counters Close S clamshells Detector checkout	DAQ/online unavailable from 0700 on for SAN reconfiguration
14	28-May	Recover from SAN reconfiguration Test PDT 033 Test L2 splitting/ORing	Run as many crates as available	Take down scaffolding in SW corner Install N veto counters Close N clamshells Complete Detector CenterBeam Survey Search and secure collision hall AD safety system tests Detector checkout Test magnet power supplies	First stuffed AFELI boards delivered	Test new latency? Collision Hall Secure Tev Beam		First store
15	4-Jun							
16	11-Jun	HEP	Collab Meeting	Collab Meeting	Collab Meeting	Collab Meeting	Collab Meeting	
17	18-Jun							
18	25-Jun							
19	2-Jul		Trigger Latency Change					
20	9-Jul							
21	16-Jul							
22	23-Jul		Routine Operation of Upgrade Systems					
23	30-Jul							
24	6-Aug							
25	13-Aug							
26	20-Aug		Manchester Workshop	Manchester Workshop	Manchester Workshop	Manchester Workshop	Manchester Workshop	
27	27-Aug							
28	3-Sep							
29	10-Sep							
30	17-Sep							
31	24-Sep						AFELI Director's Milestone	
32	1-Oct							