

## **Update to generate 22-March-2006 version**

Status the schedule to reflect recent developments

Increase anticipated duration of the L0 electrical tests prior to installation of inner H disks by one day.

Increase anticipated duration of cabling of DFEA2 crates.

## **Update to generate 16-March-2006 version**

Increase anticipated duration of the Run IIa beampipe extraction

Add columns Start 2 and Finish 2, which are intended to be copies of the pre-shutdown start and finish forecast dates (two shift activities)

Status the schedule (which requires eliminating some links and changing some task durations).

Renumber WBS tasks

## **Update to generate 14-March-2006 version**

Incorporate latest input from L1Cal team regarding installation plan

Incorporate input from L1CTT team

Include baseline for Layer 0, L1Cal and L1CTT which match pre-shutdown PMG milestones

## **Update to generate 3-March-2006 version**

Incorporate latest input from L1Cal team regarding installation schedule

## **Update to generate 1-March-2006 version**

Rename the drain coolant task to reflect the new plan to redirect coolant flow

Update plan for extraction of Lumi monitors to match latest understanding

Update a few resources to reflect latest information, and correct errors

## **Updates to generate 27-Feb-2006 version**

Modify notes to indicate resuming coolant flow after H disk removal

Reorder L1Cal tasks to improve readability.

## **Updates to generate 23-Feb-2006 version**

Modify start date to incorporate latest insights into Tevatron status

Adjust links to start of L1Cal task to proper reflect proposal to power down TFW on 6 March 2006

## **Updates to generate 22-Feb-2006 version**

Begin warm-up of silicon a day earlier to insure that it is ready for the tedlar to be removed

Complete the splitting of the H disk removal into independent north and south tasks to allow the Be beampipe wipe to be moved a day earlier.

Include an explicit task to transport Layer 0 to DAB

Update titles on a few roll-up tasks to reflect adapter card schedule updates

Include half day synchronization contingency tasks to force specific tasks to start at the beginning of a day

Update survey task duration to reflect proper estimate

Update resources in L1CTT upgrade section of schedule

Renumber the WBS

## **Updates to generate 16-Feb-2006 version**

Explicitly include a step to check capacitance between Layer 0 and beampipe

Split inner H disk removal task into individual North and South tasks to facilitate earlier start and activity in parallel with north gap adapter card effort

Explicitly show Layer 0 dry run as in parallel with South gap adapter card activity

Insure that welding of EC stub is on the day shift

Update predecessors appropriately

Update resources for the Layer 0 installation tasks

Eliminate several duplicate University/Lab tasks

Explicitly include the AD tour (as a reminder)

**Version 8-Feb-2006**