



REV.	DESCRIPTION	DRAWN	DATE
		APPD.	DATE
B.	REDUCED CIRCUIT WIDTH IN WARM INTERCEPT REGION, CREATED STAGGERED BIAS VIA ARRAY, INCREASED SIGNAL TRACE PITCH MID-CIRCUIT.	M. MATULIK	05/01/98
C.	RESTORED WARM INTERCEPT REGION WIDTH, INCREASED POWER TAIL WIDTH.	M. MATULIK	01/13/99
D.	INCREASED LENGTH BY 0.250 INCHES FOR BACKSIDE BIAS CONTACT POSITIONING	M. MATULIK	02/11/99
E.	REDUCED LENGTH BY 0.100, REDIMENSIONED DRAWING.	M. MATULIK	05/28/99

J2:01, PS03   J2:02, PS04   J2:03, PS02   J2:04, PS01  
 J2:05, PS06   J2:06, PS05   J2:07, PS07   J2:08, PS08

J3:01, CH81   J3:02, CH83   J3:03, CH84  
 J3:04, BIAS   J3:05, CH87   J3:06, CH82  
 J3:07, CH88   J3:08, CH86   J3:09, CH85  
  
 J3:10, CH71   J3:11, CH73   J3:12, CH74  
 J3:13, BIAS   J3:14, CH77   J3:15, CH72  
 J3:16, CH78   J3:17, CH76   J3:18, CH75  
  
 J3:19, CH61   J3:20, CH63   J3:21, CH64  
 J3:22, BIAS   J3:23, CH67   J3:24, CH62  
 J3:25, CH68   J3:26, CH66   J3:27, CH65  
  
 J3:28, CH51   J3:29, CH53   J3:30, CH54  
 J3:31, BIAS   J3:32, CH57   J3:33, CH52  
 J3:34, CH58   J3:35, CH56   J3:36, CH55  
  
 J3:37, CH41   J3:38, CH43   J3:39, CH44  
 J3:40, BIAS   J3:41, CH47   J3:42, CH42  
 J3:43, CH48   J3:44, CH46   J3:45, CH45  
  
 J3:46, CH31   J3:47, CH33   J3:48, CH34  
 J3:49, BIAS   J3:50, CH37   J3:51, CH32  
 J3:52, CH38   J3:53, CH36   J3:54, CH35  
  
 J3:55, CH21   J3:56, CH23   J3:57, CH24  
 J3:58, BIAS   J3:59, CH27   J3:60, CH22  
 J3:61, CH28   J3:62, CH26   J3:63, CH25  
  
 J3:64, CH11   J3:65, CH13   J3:66, CH14  
 J3:67, BIAS   J3:68, CH17   J3:69, CH12  
 J3:70, CH18   J3:71, CH16   J3:72, CH15

ITEM	PART NUMBER	DESCRIPTION OR SIZE	QTY
<b>PARTS LIST</b>			
UNLESS OTHERWISE SPEC.		ORIGINATOR	M. Matulik
		DRAWN BY	M. Matulik
		CHECKED BY	
		APPROVED BY	
		USED ON -	
		MATERIAL -	
 <b>FERMI NATIONAL ACCELERATOR LABORATORY</b> UNITED STATES DEPARTMENT OF ENERGY			
<b>VLPC 64 Channel Readout Cable</b> Model J Left Hand Connector Pad Identification			
SCALE	FILMED	DRAWING NUMBER	REV.
		3823.113-MB-330202	E

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